

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics and Communication Engineering

24/04/2014

AGENDA

- 1 Finalization of Schema & Syllabus for B.Tech Course for the Academic Year 2014-15
- 2 Finalization of Schema & Syllabus for M.Tech(DSCE), M.Tech(VLSI System Design) and M.Tech(ES) Courses for the Academic Year 2014-15.
- 3 Identification of Examiners and Valuators list.
- 4 Finalization of one common subject for Non ECE Branch Students as Elective.
- 5 Any other point



BoS Chairman-ECE

**MALLA REDDY ENGINEERING COLLEGE
(AUTONOMOUS)**

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

MINUTES OF BOS MEETING HELD ON 26TH April 2014 IN ECE DEPARTMENT

1. The Syllabus for MR14 batch have been approved with the following suggestions

Year	Semester	Name of the Subject	Modifications Suggested
II	I	EDC	“Review of transport phenomenon in semiconductors” topic is added.
II	II	ECA	“MOSFET Amplifier” topic is introduced.
II	II	PDC	“Slope Error” topic for Time Base Generators is added.
II	II	DD Using HDL	Syllabus is to be framed according to Wakerly Text book.
II	II	PDC Lab	Five HDL experiments are added in PDC lab.
III	I	LDIC	Second order Active filter, Current Steering DACs, Sigma-Delta Converters are added.
III	II	MPMC	8259(Interrupt controller), 8237(DMA Controller) interfacing topics are introduced.
III	II	DSP	Unit I is removed, Realization of structures (IIR,FIR) and Architecture for programmable DSP devices are added.
III	II	DSP Lab	5 of the experiments to be done using Hardware.
IV	I	TVE	Digital TV techniques to be elaborated (LCD, LED, HDTV, and PLASMA).

- 2 After formulation of scheme of Instructions, the BOS members reviewed and approved new courses.

The following subjects are introduced for the academic year 2014-15 (MR14).

- Basic Simulation Lab
- Human Values and Professional Ethics
- Disaster Management
- Intellectual Property Rights
- Multimedia and Signal Coding
- Embedded System Design

3. The structure and syllabus for M. Tech (VLSI & ES) have been approved with few modifications as follows.

- i) “VLSI Technology and Design” is replaced by “CMOS Digital IC Design”.
- ii) “Embedded C” Syllabus is to be framed based on ARM plat form.

4. The structure and syllabus for M. Tech (DSCE) have been approved for 2014-15 admitted batch.

5. The structure and syllabus for M. Tech (VLSI) have been approved for 2014-15 admitted batch.

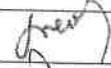


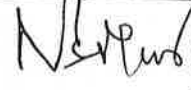



6. The structure and syllabus for M. Tech (ES) have been approved for 2014-15 admitted batch.

7. Syllabus for "Electronic Circuits" for B Tech (EEE) has been approved

8. "Microprocessor & Microcontrollers" is offered to B Tech (CSE) instead of Microprocessor & Interfacing.

9. The structure and syllabus for B.Tech have been approved for 2014-15 admitted batch with some modifications as listed in above table.

The following members are present the BOS meeting

S. No.	Name of the Member	Designation & Official Address	Signature
1	Dr. S. Sudhakara Reddy	Principal & Constituted BoS	
2	Dr. M. Ch. P. Jagdish	Chairman- BoS Profesor & HOD-ECE	
3	Dr. D. Srinivasa Rao	Profesor & HOD-ECE- JNTUHCEH	 26/4/2014
4	Dr. N. S. Murthy	Dean & Professor of ECE - NIT Warangal & Subject Expert - 1	 26/4/14
5	Sri.S.Nagendra	Director,Unistring Tech Solutions(Pvt) Ltd,Hyderabad & Industry Expert	
6	Prof.K.V.Ram Prasad	Professor, ECE Department, MREC	
7	Mrs. G. Sree Lakshmi	Assoc Professor of ECE- MREC	
8	Mr. P.Ashok Babu	Assoc. Professor of ECE- MREC	
9	Mrs.P.S.Indrani	Assoc Professor of ECE- MREC	