



# Malla Reddy Engineering College

(AUTONOMOUS)

(An UGC Autonomous Institution approved by AICTE and affiliated to JNTU Hyderabad, Accredited by NAAC with 'A' Grade (II - cycle)  
NBA Accredited Programmes - UG (CE, EEE, ME, ECE & CSE) PG (CE - Structural Engg., EEE-Electrical Power Systems, ME - Thermal Engg.).

## ORDER

Date:05/02/2021

As per UGC guidelines, the ECE UG-Board of Studies for the Academic year 2020-21 have been reconstituted as per some faculty members have been relieved from the institute.

S. No.	Name of the Member	Designation & Official Address	Subject Specialization	Contact No.	Email ID	Category
1	Dr. MadhuBabuSikha	Associate Professor and HoD, Dept. of ECE, MREC (A)	Communication Systems	9042857114	sikha.madhu@gmail.com	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	Computer Networks	9441189228	dsraoece@jntuh.ac.in	University Nominee (Nominated by the ViceChancellor, JNTUH,Hyderabad)
3	Dr. B. RajendraNaik	Professor, Dept. of ECE, OU Hyderabad.	VLSI & Image Processing	9441222226	rajendranaikb@osmania.ac.in	Subject Expert (outside theParent University) Member
4	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Signal processing	8332969363	ravikumar@nitw.ac.in	Subject Expert (outside theParentUniversity) Member
5	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Embedded	7799855887	Shalowm.devraj@gmail.com	Industry Expert
6	Dr. T. Swapna	Professor	Embedded Systems	9948195593	swapnathouti@gmail.com	Faculty Member (Internal)
7	Dr. T.Srinivas Reddy	Associate Professor	Image processing	9492571803	srimerc.2014@gmail.com	Faculty Member (Internal)
8	Dr. GSK Gayatri Devi	Associate Professor	Antennas	9490301962	gayatrigavalapu@gmail.com	Faculty Member(Internal)
9	Dr. A. Pradeep Kumar	Associate Professor	VLSI System Design	6281492032	pradeepsujeeth@gmail.com	Faculty Member (Internal)
10	Ms. C. Silpa	Associate Professor	Communications	9490421721	srsilpavasu@gmail.com	Faculty Member (Internal)
11	Mr J. Sunil Kumar	Assistant Professor	Systems and Signal Processing	7032677567	kmrsja@gmail.com	Faculty Member (Internal)
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Communications	7036013949	raghavendarr08@gmail.com	Alumni (MR13 batch)



*Raveendh*  
Principal  
Malla Reddy Engineering College  
(Autonomous)  
Maisammaguda, Dhulapally,  
(Post Via Kompally), Sec'bad-500 100.



MREC-DA-AA-MM  
9.docx

**MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)**  
(An UGC Autonomous Institution, Approved by AICTE, New Delhi & Affiliated to  
JNTUH, Hyderabad). Accredited 2<sup>nd</sup> time by NAAC with 'A' Grade,  
Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,  
Telangana State – 500100, [www.mrec.ac.in](http://www.mrec.ac.in)

## Department of Electronics and Communication Engineering

Date: 06-02-2021

### AGENDA:

1. Action taken report on BoS on 18.08.2020.
2. Discussion and Review of B.Tech-ECE Second year course schema and syllabus for MR20 regulations (2020 admitted batch onwards)
3. Approval for inclusion of Fundamentals of Data Structures Lab in the Semester-III of B.Tech.- ECE.
4. Approval for addition of Object Oriented Programming through Java Lab in the Semester-IV of B.Tech.- ECE
5. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.
6. Suggestion and review on Innovative Teaching Practices in view of Online Classes.
7. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

  
BoS, Chairman

(Dr.S.Madhubabu)

**MALLA REDDY ENGINEERING COLLEGE (Autonomous)**  
Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.  
Department of Electronics and Communication Engineering

**Minutes of Meeting B. Tech (ECE) - BOS**

6<sup>th</sup> February, 2021

The Board of Studies meeting for the B. Tech (ECE) - BOS was conducted on 6<sup>th</sup> February 2021 at 10:00 A.M., Malla Reddy Engineering College (Autonomous), Hyderabad.

The following was the agenda for the meeting.

1. Action taken report on BoS on 18.08.2020.
2. Discussion and Review of B.Tech-ECE Second year course schema and syllabus for MR20 regulations (2020 admitted batch onwards)
3. Approval for inclusion of Fundamentals of Data Structures Lab in the Semester-III of B.Tech.- ECE.
4. Approval for addition of Object Oriented Programming through Java Lab in the Semester-IV of B.Tech.- ECE
5. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.
6. Suggestion and review on Innovative Teaching Practices in view of Online Classes.
7. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

at the outset, Dr. S. Madhu Babu, Asst. Professor, HOD/ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, welcomed the members to the Board of Studies meeting and appraised the purpose of the meeting. He also briefed about the previous BOS meeting, resolutions. Later Board of studies members have in detail discussed reviewed each item of the proposed agenda and finally the following items have been resolved unanimously by all the members of BOS for their implementation from the academic year 2020-21 and onwards.

**The following suggestions were made by the members of the Board of studies.**

1 The previous Board of studies for UG meeting for AY 2020-21 was held on dated 18<sup>th</sup> Aug 2020. The following are the resolutions made in the meeting and corresponding actions taken:

1	Resolution
	The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for the first year B.Tech. (ECE) Course as per the guidelines of the AICTE and JNTUH, Hyderabad. The BOS has collectively approved and resolved to implement this scheme of Instructions, periods per week and number of credits for the first year of B.Tech. (ECE) Courses from the

		academic year 2020-21 onwards.
	<b>Action Taken</b>	The devised scheme of instructions is implemented as MR20 regulation B.Tech. (ECE) course schema of instructions and is implemented from the academic year 2020-21 onwards.
2	<b>Resolution</b>	After formulation of scheme of Instructions, the BOS has reviewed and devised the first year syllabus for B.Tech. (ECE) Course and approved as per the guidelines of the UGC, AICTE and JNTUH, Hyderabad.
	<b>Action Taken</b>	The devised syllabus is implemented as MR20 regulations B.Tech. (ECE) course syllabus and is implemented from the academic year 2020-21 onwards.
3	<b>Resolution</b>	BoS members have suggested to offer Internet of Things course, instead of Low power VLSI design under Professional Elective-IV in Semester-VII of MR 18 regulations.
	<b>Action Taken</b>	Implemented the suggestion from AY 2021-22.
4	<b>Resolution</b>	Internship-I, II and III, NPTEL-I and II need not to be mentioned in Course schema, instead mention about them regulations. (OR) If you mention internship in schema, don't give number for it
	<b>Action Taken</b>	Implemented the suggestion, all the Internships and NPTELS are not being mentioned in course schema.
5	<b>Resolution</b>	In schema, differentiate between theory and practical courses
	<b>Action Taken</b>	Implemented the suggestion.
6	<b>Resolution</b>	For all the labs related to computer science engineering, don't give Tutorial hours, instead give Practical courses.
	<b>Action Taken</b>	Implemented the suggestion.
7	<b>Resolution</b>	BoS members have suggested to swap the order of LDICA and MPMC in Semester-V
	<b>Action Taken</b>	Implemented the suggestion.
8	<b>Resolution</b>	<ul style="list-style-type: none"> <li>• In Professional Elective-I, "Artificial neural networks" course may not be required. Instead move "Machine learning" course which is in Professional Elective-II.</li> <li>• Move "Electronic Measurements and Instrumentation" course from PE-I to PE-II.</li> <li>• Offer "Computer Organization and Operating Systems" course in PE-I.</li> </ul>
	<b>Action Taken</b>	Implemented the suggestions.
9	<b>Resolution</b>	In Professional Elective-V, CAD for VLSI Design and in PE-VI, Wavelets and its applications may be replaced with Data Analytics and Adhoc Wireless Sensor Networks courses.
	<b>Action Taken</b>	Implemented the suggestion.

- 2 BoS members have suggested to interchange fundamentals of data structures lab and basic python programming lab.
- 3 BoS members have unanimously approved to introduce fundamentals of data structures lab and object oriented programming through java lab in III-sem and IV-sem respectively.
- 4 In MR20, Sem-V, interchange the positions of LDICA and MPMC.
- 5 In mr20 sem- VIII, introduce Comprehensive Vivavoce (CVV) instead of seminar.

Finally, Dr. Madhu Babu Sikha, Assoc. Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution.

**The following BOS members have attended the meeting through Offline:**

Sl.No	Name	Designation & Official Address	Category	Signature
1	Dr. Madhu Babu Sikha	Associate Professor and HoD, Dept. of ECE, MREC (A)	Chairman- BOS	
2	Dr. T. Swapna	Professor	Faculty Member	
3	Dr. T.Srinivas Reddy	Associate Professor	Faculty Member	
4	Dr. GSK Gayatri Devi	Associate Professor	Faculty Member	
5	Dr. A. Pradeep Kumar	Associate Professor	Faculty Member	
6	Ms. C. Silpa	Associate Professor	Faculty Member	
7	Mr J. Sunil Kumar	Assistant Professor	Faculty Member	

**The following BOS members have attended the meeting through Online:**

8	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
9	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	Subject Expert (outside the Parent University) Member
10	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Subject Expert (outside the Parent University) Member
11	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Industry Expert
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Alumni (MR13 Batch)

**Note: The above BoS Members have given approval through mail**

# MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.  
Department of Electronics & Communication Engineering

## Action Taken Report (ATR) on previous BoS Meeting held on 18 Aug 2020

Date: 29-12-2020

The previous Board of studies for UG meeting for AY 2020-21 was held on dated 18<sup>th</sup> Aug 2020. The following are the resolutions made in the meeting and corresponding actions taken:

1	<b>Resolution</b>	The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for the first year B.Tech. (ECE) Course as per the guidelines of the AICTE and JNTUH, Hyderabad. The BOS has collectively approved and resolved to implement this scheme of Instructions, periods per week and number of credits for the first year of B.Tech. (ECE) Courses from the academic year 2020-21 onwards.
	<b>Action Taken</b>	The devised scheme of instructions is implemented as MR20 regulation B.Tech. (ECE) course schema of instructions and is implemented from the academic year 2020-21 onwards.
2	<b>Resolution</b>	After formulation of scheme of Instructions, the BOS has reviewed and devised the first year syllabus for B.Tech. (ECE) Course and approved as per the guidelines of the UGC, AICTE and JNTUH, Hyderabad.
	<b>Action Taken</b>	The devised syllabus is implemented as MR20 regulations B.Tech. (ECE) course syllabus and is implemented from the academic year 2020-21 onwards.
3	<b>Resolution</b>	BoS members have suggested to offer Internet of Things course, instead of Low power VLSI design under Professional Elective-IV in Semester-VII of MR 18 regulations.
	<b>Action Taken</b>	Implemented the suggestion from AY 2021-22.
4	<b>Resolution</b>	Internship-I, II and III, NPTEL-I and II need not to be mentioned in Course schema, instead mention about them regulations. (OR) If you mention internship in schema, don't give number for it
	<b>Action Taken</b>	Implemented the suggestion, all the Internships and NPTELS are not being mentioned in course schema.
5	<b>Resolution</b>	In schema, differentiate between theory and practical courses
	<b>Action Taken</b>	Implemented the suggestion.
6	<b>Resolution</b>	For all the labs related to computer science engineering, don't give Tutorial hours, instead give Practical courses.
	<b>Action Taken</b>	Implemented the suggestion.
7	<b>Resolution</b>	BoS members have suggested to swap the order of LDICA and MPMC in Semester-V
	<b>Action Taken</b>	Implemented the suggestion.
8	<b>Resolution</b>	<ul style="list-style-type: none"><li>• In Professional Elective-I, "Artificial neural networks" course may not be required. Instead move "Machine learning" course which is in Professional Elective-II.</li><li>• Move "Electronic Measurements and Instrumentation" course from PE-I to PE-II.</li><li>• Offer "Computer Organization and Operating Systems" course in PE-I.</li></ul>
	<b>Action Taken</b>	Implemented the suggestions.

9	<b>Resolution</b>	In Professional Elective-V, CAD for VLSI Design and in PE-VI, Wavelets and its applications may be replaced with Data Analytics and Adhoc Wireless Sensor Networks courses.
	<b>Action Taken</b>	Implemented the suggestion.



**Dr. Madhu Babu Sikha**  
Chairman, ECE (UG)-BoS



ECE HOD &lt;ecehod@mrec.ac.in&gt;

**MREC ECE UG BoS (held on 6th Feb 2021) Minutes of the Meeting**

7 messages

Wed, Mar 31, 2021 at 1:48 PM

ECE HOD &lt;ecehod@mrec.ac.in&gt;

To: duggirala srinivasarao &lt;dsraoece@gmail.com&gt;, dsraoece@jntuh.ac.in, "Dr.B.Rajendra Naik" &lt;rajendranaikb@osmania.ac.in&gt;, Shalowm.devraj@gmail.com, raghavendarr08@gmail.com

Hello Sir,

I am attaching the UG BoS Minutes of the Meeting for your reference and perusal.

**BoS minutes are given below for quick reference. Please go through them and kindly approve.**

1. BoS members have suggested to interchange fundamentals of data structures lab and basic python programming lab.
2. BoS members have unanimously approved to introduce fundamentals of data structures lab and object oriented programming through java lab in III-sem and IV-sem respectively.
3. In MR20, Sem-V, interchange the positions of LDICA and MPMC.
4. In MR20 sem- VIII, introduce Comprehensive Vivavoce (CVV) instead of seminar.

The resolutions of these minutes will be discussed in the next BoS meeting (tentatively will be held in Aug 2021).

Regards  
Dr.M.J.C.Prasad  
Head,ECE Department  
Malla Reddy Engineering College(Autonomous), Main Campus,  
Maisammaguda,Dhulapally,  
Secunderabad, Telangana-500100

 **Minutes of Meeting BoS (ECE) 2020-21-06-02-2021.doc**  
61K

Wed, Mar 31, 2021 at 2:12 PM

duggirala srinivasarao <dsraoece@gmail.com>  
To: ECE HOD <ecehod@mrec.ac.in>

Approved

Thanks & Regards  
Dr. D: SREENIVASA RAO  
Professor , Dept. of ECE,  
JNTUHCEH, Hyderabad

[Quoted text hidden]

Thu, Apr 1, 2021 at 5:56 PM

Kanduri Raghavendar <raghavendarr08@gmail.com>  
To: ECE HOD <ecehod@mrec.ac.in>



4/12/2021

MREC Mail - MREC ECE UG BoS (held on 6th Feb 2021) Minutes of the Meeting

Malla Reddy Engineering College(Autonomous), Main Campus,  
Maisammaguda,Dhulapally,  
Secunderabad, Telangana-500100

[Quoted text hidden]

Fri, Apr 2, 2021 at 11:30 AM

**ECE HOD** <ecehod@mrec.ac.in>

To: Kanduri Raghavendar <raghavendarr08@gmail.com>

Thanka a lot  
Regards  
Dr.M.J.C.Prasad  
Head,ECE Department  
Malla Reddy Engineering College(Autonomous), Main Campus,  
Maisammaguda,Dhulapally,  
Secunderabad, Telangana-500100

[Quoted text hidden]