Malla Reddy Engineering College

(An UGC Autonomous Institution approved by AICTE and affiliated to JNTU Hyderabad, Accredited by NAAC with 'A' Grade (II - cycle)
NBA Accredited Programmes - UG (CE, EEE, ME, ECE & CSE) PG (CE - Structural Engg., EEE-Electrical Power Systems, ME - Thermal Engg.).

ORDER

Date:14/08/2020

As per UGC guidelines, the ECE UG-Board of Studies for the Academic year 2020-21have been reconstituted as per some faculty members have been relieved from the institute.

S. No.	Name of the Member	Designation & Official Address	Subject Specialization	Contact No.	Email ID	Category
1	Dr. Madhu Babu Sikha	Assistant Professor and HoD, Dept. of ECE, MREC (A)	Communication Systems	9042857114	ecehod@mrec.ac.in	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	Computer Networks	9441189228	dsraoece@jntuh.ac.in	University Nominee (Nominated by the ViceChancellor, JNTUH,Hyderabad)
3	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	VLSI & Image Processing	9441222226	rajendranaikb@osmania.ac.in	Subject Expert (outside theParent University) Member
4	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Signal processing	8332969363	ravikumar@nitw.ac.in	Subject Expert (outside theParentUniversity) Member
5	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Embedded	7799855887	Shalowm.devraj@gmail.com	Industry Expert
6	Dr. T. Swapna	Professor	Embedded Systems	9948195593	swapnathouti@gmail.com	Faculty Member (Internal)
7	Dr. T.Srinivas Reddy	Associate Professor	Image processing	9492571803	srimrec.2014@gmail.com	Faculty Member (Internal)
8	Dr. GSK Gayatri Devi	Associate Professor	Antennas	9490301962	gayatrigavalapu@gmail.com	Faculty Member(Internal)
9	Dr. A. Pradeep Kumar	Associate Professor	VLSI System Design	6281492032	pradeepsujeeth@gmail.com	Faculty Member (Internal)
10	Ms. C. Silpa	Associate Professor	Communications	9490421721	srsilpavasu@gmail.com	Faculty Member (Internal)
11	Mr J. Sunil Kumar	Assistant Professor	Systems and Signal Processing	7032677567	kmrsja@gmail.com	Faculty Member (Internal)
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Communications	7036013949	raghavendarr08@gmail.com	Alumni (MR13 batch)

Principal

PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-50

Maisammaguda, Dhulapally (Post. Via. Kompally), Medchal - Malkajgiri - 500 100.
Ph: 09348161125, 09348161303

www.mrec.ac.in
email: principal@mrec.ac.in

MALLA REDDY ENGINEERING COLLEGE

(Autonomous)

Maisammaguda, Dhullapally (post via Kompally) Secunderabad-500100

Department of Electronics & Communication Engineering

Date: 04.08.2020

CIRCULAR

The Board of Studies meeting for the Department of Electronics & Communication Engineering is scheduled on **18.08.2020** at 10.00 AM through online mode. All the BoS members are requested to attend meeting .

HOD ECE

HEAD, Dept. Of ECE
Malla Reddy Engg. College
Maisammaguda, Dhullapally.
Secunderabad

Department of Electronics and Communication Engineering

The Agenda of the BoS meeting for UG is as follows:

Date: 18-08-2020

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

3. Discussion and Review of First year course schema for MR20 regulations (2020 admitted batch onwards)

4. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

BoS, Chairman

(Dr.S.Madhubabu)
HEAD, Dept. of ECE
Malla Reddy Engg. College
Maisammaguda, Dhullapally.
Secunderabad

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics and Communication Engineering

Minutes of Meeting B. Tech (ECE) - BOS

18th August, 2020

The Board of Studies meeting for the B. Tech (ECE) - BOS was conducted on 18th August 2020 at 2:00 P.M. through online, Malla Reddy Engineering College (Autonomous), Hyderabad.

The following BOS members have attended the meeting:

1	Dr. Madhu Babu Sikha	Assistant Professor and HoD, Dept. of ECE, MREC (A)	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
3	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	Subject Expert (outside the Parent University) Member
4	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Subject Expert (outside the Parent University) Member
5	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Industry Expert
6	Dr. T. Swapna	Professor	Faculty Member
7	Dr. T.Srinivas Reddy	Associate Professor	Faculty Member
8	Dr. GSK Gayatri Devi	Associate Professor	Faculty Member
9	Dr. A. Pradeep Kumar	Associate Professor	Faculty Member
10	Ms. C. Silpa	Associate Professor	Faculty Member
11	Mr J. Sunil Kumar	Assistant Professor	Faculty Member (Internal)
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Alumni (MR13 Batch)

The following was the agenda for the meeting.

1. Action taken report on BoS on 30.11.2019

12. In Professional Elective-I, "Artificial neural networks" course may not be required. Instead move "Machine learning" course which is in Professional Elective-II.

13. Move "Electronic Measurements and Instrumentation" course from PE-I to PE-II.

14. Offer "Computer Organization and Operating Systems" course in PE-I.

15. LDICA and corresponding lab may be pushed to 4th semester. DSP and corresponding lab may be pushed to 5th semester, so that DIP and corresponding lab can be offered in 6th semester. Then Deep learning and its applications can be offered in PE-III (in 7th semester).

16. In PE-V, CAD for VLSI Design and in PE-VI, Wavelets and its applications may be replaced with Data Analytics and Adhoc Wireless Sensor Networks courses.

Finally, Dr. Madhu Babu Sikha, Asst. Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution.

Dr. Madhu Babu Sikha   Dr. D.Srinivasa Rao   Dr. B. Rajendra Naik   Dr. J Ravi Kumar

B. Devraj   Dr. T. Swapna   Dr. T.Srinivas Reddy   Dr. G.S.K.Gayatri Devi

Dr.A.Pradeep Kumar   Ms. C. Silpa   Mr J. Sunil Kumar   Mr. Raghavendar

MREC ECE UG BoS Minutes of the Meeting

7 messages

ECE HOD <ecehod@mrec.ac.in>
Tue, Sep 1, 2020 at 1:19 PM
To: dsraoece@jntuh.ac.in
Cc: Ravi Kumar J <ravikumar@nitw.ac.in>, "Dr.B.Rajendra Naik" <rajendranaikb@osmania.ac.in>,
Shalowm.devraj@gmail.com, raghavendarr08@gmail.com

Hello Sir,

I am attaching the UG BoS Minutes of the Meeting for your reference and perusal.

The agenda of the BoS meeting was to get the approval for the First year course schema and syllabus. As far as
suggestions related to the First year schema was concerned, BoS members suggested to move BEEE Course to the 1st
semester, so that there will be continuity for Digital Electronics course in 2nd semester.
I spoke with other HoDs in this regard, and it is becoming very difficult to adjust their schema (because this year we have
9 CSE and allied branches) and hence we are retaining BEEE course in 2nd semester itself.
We will make sure that there is no prerequisite for the Digital electronics course.

Kindly approve the attached 1st year course schema.

Note: For the 2nd, 3rd and 4th years Schema, we will get the approval in the December 2020 BoS meeting. We will
work on the suggestions given by all the BoS members and try to incorporate them.

I request you to please reply your consent to the First year course schema.

Regards
HOD-ECE
Malla Reddy Engineering College(Autonomous), Main Campus,
Maisammaguda,Dhulapally,
Secunderabad, Telangana-500100

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3 attachments

📝 **ECE BoS minutes.docx**
17K

📕 **BoS ppt Aug 2020.pdf**
549K

📝 **MR20 ECE 1st year course schema.docx**
23K

Ravi Kumar J <ravikumar@nitw.ac.in>
Tue, Sep 1, 2020 at 2:27 PM
To: ECE HOD <ecehod@mrec.ac.in>
Cc: dsraoece@jntuh.ac.in, "Dr.B.Rajendra Naik" <rajendranaikb@osmania.ac.in>, Shalowm.devraj@gmail.com,
raghavendarr08@gmail.com

Please shift Basic electronics and electrical engineering in first sem as we discussed
[Quoted text hidden]

Prof.B.RAJENDRA NAIK <rajendranaikb@osmania.ac.in>
Fri, Oct 16, 2020 at 3:16 PM
To: Ravi Kumar J <ravikumar@nitw.ac.in>
Cc: ECE HOD <ecehod@mrec.ac.in>, dsraoece@jntuh.ac.in, Shalowm.devraj@gmail.com, raghavendarr08@gmail.com

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics & Communication Engineering

Action Taken Report (ATR) on previous BoS Meeting held in AY 2019-20

Date: 18-08-2020

The previous Board of studies (UG) meeting for AY 2019-20 was held on dated 30th Nov 2019. The following are the resolutions made in the meeting and corresponding actions taken:

1	Resolution	The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for all 4 years of B.Tech. (ECE) Course as per the guide lines of the AICTE and JNTUH, Hyderabad. The BOS has collectively approved and resolved to implement this scheme of Instructions, periods per week and number of credits for all 4 years of B.Tech. (ECE) Courses from the academic year 2018-19 onwards.
	Action Taken	The devised scheme of instructions is implemented as MR18 regulation B.Tech. (ECE) course schema of instructions and is implemented from the academic year 2018-19 onwards.
2	Resolution	After formulation of scheme of Instructions, the BOS has reviewed and devised the syllabus for B.Tech. (ECE) Course and approved as per the guide lines of the UGC, AICTE and JNTUH, Hyderabad with 160 credits.
	Action Taken	The devised syllabus is implemented as MR18 regulation B.Tech. (ECE) course syllabus with 160 credits is implemented from the academic year 2018-19 onwards.
3	Resolution	The BoS members unanimously approved to introduce the following in MR18 schema: • Quantitative Aptitude-I and Quantitative Aptitude-II courses in Semester V and Semester-VI, respectively. • Object Oriented Programming Through Java instead of Indian Constitution in Semester-V • Fundamentals of Database Management Systems instead of Essence of Indian Traditional Knowledge in Semester VI of Mr18 schema
	Action Taken	Implemented from the academic year 2020-21.
4	Resolution	If any item needs to be modified /added/ deleted, the same may be circulated by E-mail to all members and after taking electronic/E-mail approval, the modification may be made operational.
	Action Taken	Implemented from the academic year 2018-19.

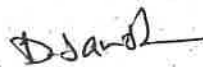Dr. Madhu Babu Sikha   Dr. D.Srinivasa Rao   Dr. B. Rajendra Naik

Dr. J Ravi Kumar   B. Devraj   Dr. T. Swapna

Dr. T.Srinivas Reddy   Ms. C. Silpa   Mr. L Sunil Kumar

Malla Reddy Engineering College
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Lr. No: MREC (A)/BOS-ECE/UG/Aug 2020-21/Invitation/04   14.08.2020

To

B. Devraj,

Accenture Services Pvt.Ltd,
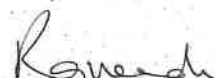
Hyderabad.

Dear Sir,

      Sub: Meeting of the Board of Studies for ECE – UG for the academic year 2020-21: Reg.

It is pleasure to invite you for the meeting of the Board of Studies for the Electronics and Communication Engineering (UG) during the academic year 2020-21. It is scheduled at 2:00 P.M. on 18th August,2020 through online, Malla Reddy Engineering College (Autonomous).

We request you to make it convenient to attend the meeting.

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

3. Discussion and Review of First year course schema for MR20 regulations (2020 admitted batch onwards)

4. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

Principal

Dr. A. Raveendra
PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100

Malla Reddy Engineering College
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Lr. No: MREC (A)/BOS-ECE/UG/Aug 2020-21/Invitation/03   14.08.2020

To

Dr. J. Ravi Kumar,

Professor, Dept. of ECE,

National Institute of Technology , Warangal

Dear Sir,

     Sub: Meeting of the Board of Studies for ECE – UG for the academic year 2020-21: Reg.

It is pleasure to invite you for the meeting of the Board of Studies for the Electronics and Communication Engineering (UG) during the academic year 2020-21. It is scheduled at 2:00 P.M. on 18th August,2020 through online, Malla Reddy Engineering College (Autonomous).

We request you to make it convenient to attend the meeting.

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

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Principal

Dr. A. Raveendra
PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
'Post Via Kompally), Sec'bad-500 100

Malla Reddy Engineering College
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Lr. No: MREC (A)/BOS-ECE/UG/Aug 2020-21/Invitation/02   14.08.2020

To

Dr. B. Rajendra Naik

Professor, Dept. of ECE,
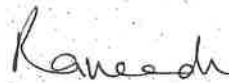
Osmania University, Hyderabad.

Dear Sir,

Sub: Meeting of the Board of Studies for ECE – UG for the academic year 2020-21: Reg.

It is pleasure to invite you for the meeting of the Board of Studies for the Electronics and Communication Engineering (UG) during the academic year 2020-21. It is scheduled at 2:00 P.M. on 18th August,2020 through online, Malla Reddy Engineering College (Autonomous).

We request you to make it convenient to attend the meeting.

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

3. Discussion and Review of First year course schema for MR20 regulations (2020 admitted batch onwards)

4. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

Principal

Dr.A.Raveendra
PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 10

Lr. No: MREC (A)/BOS-ECE/UG/Aug 2020-21/Invitation/01   14.08.2020

To

Dr. D. Srinivasa Rao,

Professor, Dept. of ECE

JNTUHCE, Hyderabad.

Dear Sir,

    Sub: Meeting of the Board of Studies for ECE – UG for the academic year 2020-21: Reg.

It is pleasure to invite you for the meeting of the Board of Studies for the Electronics and Communication Engineering (PG) during the academic year 2020-21. It is scheduled at 2:00 P.M. on 18th August 2020 through online, Malla Reddy Engineering College (Autonomous).

We request you to make it convenient to attend the meeting.

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

3. Discussion and Review of First year course schema for MR20 regulations (2020 admitted batch onwards)

4. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

Principal

Dr. A. Raveendra
PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100

Malla Reddy Engineering College
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NBA Accredited Programmes - UG (CE, EEE, ME, ECE & CSE) PG (CE - Structural Engg., EEE-Electrical Power Systems, ME - Thermal Engg.).

Lr. No: MREC (A)/BOS-ECE/UG/Aug 2020-21/Invitation/05   14.08.2020

To

Mr. Raghavender,

System Engineer,

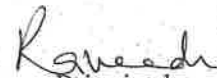**Infosys,**

Hyderabad

Dear Sir,

Sub: Meeting of the Board of Studies for ECE – UG for the academic year 2020-21: Reg.

It is pleasure to invite you for the meeting of the Board of Studies for the Electronics and Communication Engineering (UG) during the academic year 2020-21. It is scheduled at 2:00 P.M. on 18th August,2020 through online, Malla Reddy Engineering College (Autonomous).

We request you to make it convenient to attend the meeting.

AGENDA:

1. Action taken report on BoS on 30.11.2019

2. Discussion and Review of Modifications in MR18 (2018 admitted batch) course schema

3. Discussion and Review of First year course schema for MR20 regulations (2020 admitted batch onwards)

4. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

Principal

Dr. A. Raveendra
PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100

Malla Reddy Engineering College

(AUTONOMOUS)

ORDER
Date:05/02/2021

As per UGC guidelines, the ECE UG-Board of Studies for the Academic year 2020-21have been reconstituted as per some faculty members have been relieved from the institute.

S. No.	Name of the Member	Designation & Official Address	Subject Specialization	Contact No.	Email ID	Category
1	Dr. MadhuBabuSikha a	Associate Professor and HoD, Dept. of ECE, MREC (A)	Communication Systems	9042857114	sikha.madhu@gmail.com	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	Computer Networks	9441189228	dsraoece@jntuh.ac.in	University Nominee (Nominated by the ViceChancellor, JNTUH,Hyderabad)
3	Dr. B. RajendraNaik	Professor, Dept. of ECE, OU Hyderabad.	VLSI & Image Processing	9441222226	rajendranaikb@osmania.ac.in	Subject Expert (outside theParent University) Member
4	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Signal processing	8332969363	ravikumar@nitw.ac.in	Subject Expert (outside theParentUniversity) Member
5	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Embedded	7799855887	Shalowm.devraj@gmail.com	Industry Expert
6	Dr. T. Swapna	Professor	Embedded Systems	9948195593	swapnathouti@gmail.com	Faculty Member (Internal)
7	Dr. T.Srinivas Reddy	Associate Professor	Image processing	9492571803	srimrec.2014@gmail.com	Faculty Member (Internal)
8	Dr. GSK Gayatri Devi	Associate Professor	Antennas	9490301962	gayatrigavalapu@gmail.com	Faculty Member(Internal)
9	Dr. A. Pradeep Kumar	Associate Professor	VLSI System Design	6281492032	pradeepsujeeth@gmail.com	Faculty Member (Internal)
10	Ms. C. Silpa	Associate Professor	Communications	9490421721	srsilpavasu@gmail.com	Faculty Member (Internal)
11	Mr J. Sunil Kumar	Assistant Professor	Systems and Signal Processing	7032677567	kmrsja@gmail.com	Faculty Member (Internal)
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Communications	7036013949	raghavendarr08@gmail.com	Alumni (MR13 batch)

PRINCIPAL

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(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100.

Maisammaguda, Dhulapally (Post. Via. Kompally), Medchal - Malkajgiri - 500 100.
Ph: 093481 61125, 08885542037

www.mrec.ac.in
email: principal@mrec.ac.in

Department of Electronics and Communication Engineering

Date: 06-02-2021

AGENDA:

1. Action taken report on BoS on 18.08.2020.

2. Discussion and Review of B.Tech-ECE Second year course schema and syllabus for MR20 regulations (2020 admitted batch onwards)

3. Approval for inclusion of Fundamentals of Data Structures Lab in the Semester-III of B.Tech.- ECE.

4. Approval for addition of Object Oriented Programming through Java Lab in the Semester-IV of B.Tech.- ECE

5. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.

6. Suggestion and review on Innovative Teaching Practices in view of Online Classes.

7. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

BoS, Chairman

(Dr.S.Madhubabu)

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics and Communication Engineering

Minutes of Meeting B. Tech (ECE) - BOS

6th February, 2021

The Board of Studies meeting for the B. Tech (ECE) - BOS was conducted on 6th February 2021 at 10:00 A.M., Malla Reddy Engineering College (Autonomous), Hyderabad.

The following was the agenda for the meeting.

1. Action taken report on BoS on 18.08.2020.

2. Discussion and Review of B.Tech-ECE Second year course schema and syllabus for MR20 regulations (2020 admitted batch onwards)

3. Approval for inclusion of Fundamentals of Data Structures Lab in the Semester-III of B.Tech.- ECE.

4. Approval for addition of Object Oriented Programming through Java Lab in the Semester-IV of B.Tech.- ECE

5. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.

6. Suggestion and review on Innovative Teaching Practices in view of Online Classes.

7. Delegation of power to the Chairman-BoS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

at the outset, Dr. S. Madhu Babu, Asst. Professor, HOD/ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, welcomed the members to the Board of Studies meeting and appraised the purpose of the meeting. He also briefed about the previous BOS meeting, resolutions. Later Board of studies members have in detail discussed reviewed each item of the proposed agenda and finally the following items have been resolved unanimously by all the members of BOS for their implementation from the academic year 2020-21 and onwards.

The following suggestions were made by the members of the Board of studies.

1 The previous Board of studies for UG meeting for AY 2020-21 was held on dated 18th Aug 2020. The following are the resolutions made in the meeting and corresponding actions taken:

| 1 | Resolution | The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for the first year B.Tech. (ECE) Course as per the guidelines of the AICTE and JNTUH, Hyderabad. The BOS has collectively approved and resolved to implement this scheme of Instructions, periods per week and number of credits for the first year of B.Tech. (ECE) Courses from the |

		academic year 2020-21 onwards.
	Action Taken	The devised scheme of instructions is implemented as MR20 regulation B.Tech. (ECE) course schema of instructions and is implemented from the academic year 2020-21 onwards.
2	**Resolution**	After formulation of scheme of Instructions, the BOS has reviewed and devised the first year syllabus for B.Tech. (ECE) Course and approved as per the guidelines of the UGC, AICTE and JNTUH, Hyderabad.
	Action Taken	The devised syllabus is implemented as MR20 regulations B.Tech. (ECE) course syllabus and is implemented from the academic year 2020-21 onwards.
3	**Resolution**	BoS members have suggested to offer Internet of Things course, instead of Low power VLSI design under Professional Elective-IV in Semester-VII of MR 18 regulations.
	Action Taken	Implemented the suggestion from AY 2021-22.
4	**Resolution**	Internship-I, II and III, NPTEL-I and II need not to be mentioned in Course schema, instead mention about them regulations. (OR) If you mention internship in schema, don't give number for it
	Action Taken	Implemented the suggestion, all the Internships and NPTELs are not being mentioned in course schema.
5	**Resolution**	In schema, differentiate between theory and practical courses
	Action Taken	Implemented the suggestion.
6	**Resolution**	For all the labs related to computer science engineering, don't give Tutorial hours, instead give Practical courses.
	Action Taken	Implemented the suggestion.
7	**Resolution**	BoS members have suggested to swap the order of LDICA and MPMC in Semester-V
	Action Taken	Implemented the suggestion.
8	**Resolution**	• In Professional Elective-I, "Artificial neural networks" course may not be required. Instead move "Machine learning" course which is in Professional Elective-II. • Move "Electronic Measurements and Instrumentation" course from PE-I to PE-II. • Offer "Computer Organization and Operating Systems" course in PE-I.
	Action Taken	Implemented the suggestions.
9	**Resolution**	In Professional Elective-V, CAD for VLSI Design and in PE-VI, Wavelets and its applications may be replaced with Data Analytics and Adhoc Wireless Sensor Networks courses.
	Action Taken	Implemented the suggestion.

2   BoS members have suggested to interchange fundamentals of data structures lab and basic python programming lab.

3   BoS members have unanimously approved to introduce fundamentals of data structures lab and object oriented programming through java lab in III-sem and IV-sem respectively.

4   In MR20, Sem-V, interchange the positions of LDICA and MPMC.

5   In mr20 sem- VIII, introduce Comprehensive Vivavoce (CVV) instead of seminar.

Finally, Dr. Madhu Babu Sikha, Assoc. Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution.

The following BOS members have attended the meeting through Offline:

Sl.No	Name	Designation & Official Address	Category	Signature
1	Dr. Madhu Babu Sikha	Associate Professor and HoD, Dept. of ECE, MREC (A)	Chairman- BOS	
2	Dr. T. Swapna	Professor	Faculty Member	
3	Dr. T.Srinivas Reddy	Associate Professor	Faculty Member	
4	Dr. GSK Gayatri Devi	Associate Professor	Faculty Member	
5	Dr. A. Pradeep Kumar	Associate Professor	Faculty Member	
6	Ms. C. Silpa	Associate Professor	Faculty Member	
7	Mr J. Sunil Kumar	Assistant Professor	Faculty Member	

The following BOS members have attended the meeting through Online:

8	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
9	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	Subject Expert (outside the Parent University) Member
10	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Subject Expert (outside the Parent University) Member
11	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Industry Expert
12	Mr. Raghavender	Systems Engineer, Infosys, Hyderabad	Alumni (MR13 Batch)

Note: The above BoS Members have given approval through mail

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics & Communication Engineering

Action Taken Report (ATR) on previous BoS Meeting held on 18 Aug 2020

Date: 29-12-2020

The previous Board of studies for UG meeting for AY 2020-21 was held on dated 18th Aug 2020. The following are the resolutions made in the meeting and corresponding actions taken:

1	Resolution	The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for the first year B.Tech. (ECE) Course as per the guidelines of the AICTE and JNTUH, Hyderabad. The BOS has collectively approved and resolved to implement this scheme of Instructions, periods per week and number of credits for the first year of B.Tech. (ECE) Courses from the academic year 2020-21 onwards.
	Action Taken	The devised scheme of instructions is implemented as MR20 regulation B.Tech. (ECE) course schema of instructions and is implemented from the academic year 2020-21 onwards.
2	Resolution	After formulation of scheme of Instructions, the BOS has reviewed and devised the first year syllabus for B.Tech. (ECE) Course and approved as per the guidelines of the UGC, AICTE and JNTUH, Hyderabad.
	Action Taken	The devised syllabus is implemented as MR20 regulations B.Tech. (ECE) course syllabus and is implemented from the academic year 2020-21 onwards.
3	Resolution	BoS members have suggested to offer Internet of Things course, instead of Low power VLSI design under Professional Elective-IV in Semester-VII of MR 18 regulations.
	Action Taken	Implemented the suggestion from AY 2021-22.
4	Resolution	Internship-I, II and III, NPTEL-I and II need not to be mentioned in Course schema, instead mention about them regulations. (OR) If you mention internship in schema, don't give number for it
	Action Taken	Implemented the suggestion, all the Internships and NPTELs are not being mentioned in course schema.
5	Resolution	In schema, differentiate between theory and practical courses
	Action Taken	Implemented the suggestion.
6	Resolution	For all the labs related to computer science engineering, don't give Tutorial hours, instead give Practical courses.
	Action Taken	Implemented the suggestion.
7	Resolution	BoS members have suggested to swap the order of LDICA and MPMC in Semester-V
	Action Taken	Implemented the suggestion.
8	Resolution	• In Professional Elective-I, "Artificial neural networks" course may not be required. Instead move "Machine learning" course which is in Professional Elective-II. • Move "Electronic Measurements and Instrumentation" course from PE-I to PE-II. • Offer "Computer Organization and Operating Systems" course in PE-I.
	Action Taken	Implemented the suggestions.

9	Resolution	In Professional Elective-V, CAD for VLSI Design and in PE-VI, Wavelets and its applications may be replaced with Data Analytics and Adhoc Wireless Sensor Networks courses.
	Action Taken	Implemented the suggestion.

Dr. Madhu Babu Sikha
Chairman, ECE (UG)-BoS

ECE HOD <ecehod@mrec.ac.in>

MREC ECE UG BoS (held on 6th Feb 2021) Minutes of the Meeting
7 messages

ECE HOD <ecehod@mrec.ac.in>   Wed, Mar 31, 2021 at 1:48 PM
To: duggirala srinivasarao <dsraoece@gmail.com>, dsraoece@jntuh.ac.in, "Dr.B.Rajendra Naik"
<rajendranaikb@osmania.ac.in>, Shalowm.devraj@gmail.com, raghavendarr08@gmail.com

Hello Sir,

I am attaching the UG BoS Minutes of the Meeting for your reference and perusal.

BoS minutes are given below for quick reference. Please go through them and kindly approve.

1. BoS members have suggested to interchange fundamentals of data structures lab and basic python programming lab.

2. BoS members have unanimously approved to introduce fundamentals of data structures lab and object oriented programming through java lab in III-sem and IV-sem respectively.

3. In MR20, Sem-V, interchange the positions of LDICA and MPMC.

4. In MR20 sem- VIII, introduce Comprehensive Vivavoce (CVV) instead of seminar.

The resolutions of these minutes will be discussed in the next BoS meeting (tentatively will be held in Aug 2021).

Regards
Dr.M.J.C.Prasad
Head,ECE Department
Malla Reddy Engineering College(Autonomous), Main Campus,
Maisammaguda,Dhulapally,
Secunderabad, Telangana-500100

📄 **Minutes of Meeting BoS (ECE) 2020-21-06-02-2021.doc**
61K

duggirala srinivasarao <dsraoece@gmail.com>   Wed, Mar 31, 2021 at 2:12 PM
To: ECE HOD <ecehod@mrec.ac.in>

Approved

Thanks & Regards
Dr. D. SREENIVASA RAO
Professor , Dept. of ECE,
JNTUHCEH, Hyderabad

[Quoted text hidden]

Kanduri Raghavendar <raghavendarr08@gmail.com>   Thu, Apr 1, 2021 at 5:56 PM
To: ECE HOD <ecehod@mrec.ac.in>

Malla Reddy Engineering College(Autonomous), Main Campus,
Maisammaguda,Dhulapally,
Secunderabad, Telangana-500100

[Quoted text hidden]

ECE HOD <ecehod@mrec.ac.in>   Fri, Apr 2, 2021 at 11:30 AM
To: Kanduri Raghavendar <raghavendarr08@gmail.com>

Thanka a lot
Regards
Dr.M.J.C.Prasad
Head,ECE Department
Malla Reddy Engineering College(Autonomous), Main Campus,
Maisammaguda,Dhulapally,
Secunderabad, Telangana-500100

[Quoted text hidden]