



Malla Reddy Engineering College

(AUTONOMOUS)

(An UGC Autonomous Institution approved by AICTE and affiliated to JNTU Hyderabad, Accredited by NAAC with 'A' Grade (II - cycle)
NBA Accredited Programmes - UG (CE, EEE, ME, ECE & CSE) PG (CE - Structural Engg., EEE-Electrical Power Systems, ME - Thermal Engg.).

ORDER

Date: 10.04.2017

The ECE Department Board-of-Studies for the academic year 2017-18 have been reconstituted for UG as per the UGC guidelines.

S. No.	Name of the Member	Designation & Official Address	Subject Specialization	Contact No.	Status
1	Dr. S. Sudhakara Reddy	Principal, MREC(A)	Mechanical Engineering	9348161125	Special Invitee
2	Dr Kesavan Gopal	Chairman- BoS Professor, ECE Department, MREC	Digital Electronics and Advanced Communication	9000368726	Chairman- BOS
3	Dr. B.N.Bandari	Professor,ECE Department JNTUHCEH, Hyderabad	Communication Engineering	9441189228	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
4	Prof. D. Krishna Reddy	Professor, ECE Dept, CBIT, Hyderabad	Mobile Communication	9866502428	Subject Expert Member (External) (Nominated by Academic Council)
5	Mrs. N Mangala Gouri	Associate Professor, ECE Department JNTUHCEH Hyderabad	Optical communications	9866502396	Subject Expert Member (External) (Nominated by Academic Council)
6	Dr G.Mallikarjun Rao	Scientist F, RCI	Image processing	9490166665	Industry Expert
7	Dr. S. Madhu Babu	Assoc. Professor, ECE Department, MREC	Communication Systems	9042857114	Faculty Member (Internal)
8	Dr. M. Kavicharan	Assoc. Professor, ECE Department, MREC	Electronics Design and Technology	8341236155	Faculty Member (Internal)
9	P.S.Indrani	Assoc. Professor, ECE Department, MREC	Digital Systems and Computer Electronics	9490224344	Faculty Member (Internal)
10	C.Silpa	Assoc. Professor, ECE Department, MREC	Systems and Signal Processing	9490421721	Faculty Member (Internal)
11	Mr C.Sai Bhaskar Raju	Associate software Engineer, Prolifics, Hyderabad	ECE	8309364380	Alumni (Nominated by the Principal)



PRINCIPAL
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Maisammaguda, Dhulapally,
Hyderabad, 500 100

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics & Communication Engineering

Date: 28-06-2017

Board of Studies Meeting on 28.06.2017 at 10.30 AM

The 8th Board of Studies meeting for ECE (UG) is being conducted on 28-06-2017 at 10.30 AM in the conference hall, Malla Reddy Engineering College (Autonomous), Hyderabad. The following is the Agenda of the meeting.

AGENDA

Discussion and approval of the scheme and Syllabus of B.Tech. – ECE (CBCS) for MR17 Regulations (for Batches starting from Academic Year 2017-18 onwards).

Discussion and approval of the syllabus for offering subjects to other Departments.

- Electronic Devices and Circuits (EEE)
 - Electronic Devices and Circuits Lab (EEE)
 - Pulse & Digital Circuits (EEE)
 - Pulse & Digital Circuits Lab (EEE)
 - Digital Electronics (EEE)
 - Linear and Digital Integrated Circuit and Applications (EEE)
 - Microprocessor & Microcontrollers (CSE & EEE)
 - Microprocessor & Microcontrollers Lab (CSE & EEE)
 - Digital Signal Processing (EEE)
 - VLSI Design (EEE)
 - Digital Design Using Verilog (CE, EEE, ME, ECE, CSE, IT and Mi.E)
 - Mine Electronics Lab (Mi.E)
 - Principles of Communication Engineering (Open Elective)
- Suggestion and approval of open source software for suitable lab experiments.
- Suggestion and approval of panel for paper setters, Evaluators for both theory and practical examinations.
- Delegation of power to the Chairman, BOS for inclusion and exclusion of any item as per the requirements.
- Approval of any other item with the permission of the Chairman.


Chairman

ECE (UG) - BOS

Head, Department of ECE,
Malla Reddy Engineering College (A)
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Secunderabad-500 100, T.G.

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

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DEPARTMENT of ELECTRONICS & COMMUNICATION ENGINEERING

Date: 28-06-2017

The 8th Board of Studies meeting for ECE(UG) was conducted on 28-06-2017 at 10.30 AM in the conference hall. The following members were present

S. No	Name of the Member	Designation & Official Address	Designation in the Board	Sign
1	Dr. S. Sudhakara Reddy	Principal, MREC(A)	Special Invitee	
2	Dr Kesavan Gopal	Professor, ECE Department, MREC	Chairman- BOS	
3	Dr. B.N.Bhandari	Professor & Director, Academic & Planning ECE Department JNTUHCEH, Hyderabad	University Nominee	 28.06.2017
4	Dr D. Krishna Reddy	Professor, ECE Dept, CBIT, Hyderabad	Subject Expert	 28/06/17
5	Mrs. N Mangala Gouri	Associate Professor, ECE Department JNTUHCEH Hyderabad	Subject Expert	 28/06/2017
6	Dr G.Mallikarjun Rao	Scientist F, RCI	Industry Expert	 28/06/2017
7	Dr.S.Madhubabu	Assoc. Professor, ECE Department, MREC	Faculty Member	 28/6/17
8	Dr.G.S.K.Gayatri Devi	Asst. Professor, ECE Department, MREC	Faculty Member	 28/6/17
9	C.Silpa	Assoc. Professor, ECE Department, MREC	Faculty Member	 28/6/17
10	P.S.Indrani	Assoc. Professor, ECE Department, MREC	Faculty Member	
11	Mr C.Sai Bhaskar Raju	Associate software Engineer, Prolifics, Hyderabad	Alumni	


Chairman

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Department of Electronics and Communication Engineering

Minutes of Meeting B. Tech (ECE) - BOS

28th June 2017

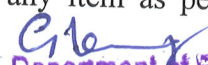
The 8th Board of Studies meeting for the B. Tech (ECE) - BOS was conducted on 28th June 2017 at 11:30 a.m. in the conference hall, Malla Reddy Engineering College (Autonomous), Hyderabad.

The following BOS members have attended the meeting:

1. Dr. S Sudhakara Reddy	Principal, MREC(A)	Special Invitee
2. Dr. Kesavan Gopal	Chairman-BOS, Professor, ECE, MREC(A)	Chairman-BOS
3. Dr. B N Bhandari	Professor & Director, Academic & Planning, ECE, JNTUHCEH, Hyderabad	University Nominee
4. Mrs. N Mangala Gouri	Assoc. Professor, ECE, JNTUHCEH, Hyderabad	Subject Expert
5. Dr. D Krishna Reddy	Professor, ECE, CBIT, Hyderabad	Subject Expert
6. Dr. G Mallikarjun Rao	Scientist F, RCI, Hyderabad	Industry Expert
7. Dr. S Madhubabu	Assoc. Professor, ECE, MREC(A)	Faculty Member
8. Dr. G S K Gayatri Devi	Asst. Professor, ECE, MREC(A)	Faculty Member
9. Mrs. C Silpa	Assoc. Professor, ECE, MREC(A)	Faculty Member
10. Mrs. P S Indrani	Assoc. Professor, ECE, MREC(A)	Faculty Member
11. Mr. C Sai Bhaskar Raju	Associate Software Engineer, Prolifics, Hyderabad	Alumni

The following was the agenda for the meeting.

1. Discussion and approval of the scheme and Syllabus of B. Tech (ECE) (CBCS) for MR17 Regulations (for Batches starting from Academic Year 2017-18 onwards).
2. Suggestion and approval of Open Source Softwares for suitable lab experiments.
3. Suggestion and approval of panel for Paper setters, Valuers for both theory and practical examinations.
4. Delegation of power to the Chairman, BOS for inclusion and exclusion of any item as per the requirements.
5. Approval of any other item with the permission of the Chairman.


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At the outset, Dr. Kesavan Gopal, Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, welcomed the members to the Board of Studies meeting and appraised the purpose of the meeting. He also briefed about the previous BOS meeting, resolutions and placed on record, the existing syllabus of MR 15 regulations and also proposed modifications for MR17 regulations syllabus (Batches admitted from 2017-18 onwards). Later Board of studies members have in detail discussed reviewed each

item of the proposed agenda and finally the following items have been resolved unanimously by all the members of BOS for their implementation from the academic year 2017-18 and onwards.


The following resolutions are made after careful discussion and observations.

1. The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for all 4 years of B. Tech (ECE) course as per the guide lines of the AICTE and JNTUH, Hyderabad. The BOS has collectively resolved to implement this scheme of Instructions, periods per week and number of credits for all 4 years of B. Tech (ECE) Course from the academic year 2017-18 onwards.
2. The Structure and Syllabus for B.Tech MR17 batch have been approved with the following suggestions

Year	Semester	Name of the Subject	Modifications Suggested
II	II	Computer Organization	New Modules are added 1. Module II - Computer Organization and Design, CPU 2. Module V –Pipeline and Vector Processing
II	II	PDC Lab	The following Experiments are added 1. Study of Flip Flop and Some Applications 2. UJT Relaxation Oscillator.
III	II	Engineering Economics & Accountancy	New Module is added Module-I Business Environment and Managerial Economics
III	II	Microcontrollers & Applications	New Modules are added 1. Module III- Real Time Control-Interrupts 2. Module IV- Real Time Control- Timer 3. Module V –Interfacing
IV	I	Embedded & Real-Time Operating Systems	New Modules are added 1. Module I-Introduction to Embedded System 2. Module II-Typical Embedded System 3. Module III-Embedded Firmware

1. After formulation of scheme of Instructions, the BOS members reviewed and approved new courses. The following subjects are introduced for the academic year 2017-18 (MR17).


Advanced Calculus
Electrical Technology
Operating Systems
Digital System Design
Data Communications and Computer Networks
MOOCs/NPTEL Certificate Course
Management Fundamentals
Telecommunication Switching Systems and Networks
Coding Theory and Techniques.
Artificial Neural Networks
Speech Processing
RF Circuit Design


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3. The BOS Members unanimously approved to use the following Open Source software for execution of programmes in the respective laboratories.
 1. GPL / GCC SDCC compiler for 8051 controllers- Microcontrollers Lab.
 2. NASM, FASM – Microprocessor and Interfacing Lab
 3. Octave Software – Basic Simulation Lab
4. The BOS members have agreed to suggest a panel for paper setters and valuers / examiners for both theory and practical of all the subjects of B. Tech (ECE) Course.
5. The BOS has also resolved for delegation of power to the Chairman of BOS for inclusion / exclusion / modification of any of the above item as per the requirements.

The following suggestions were made by the members of the Board of studies.

1. The syllabus for BEEE course should be comprised of ECE and EEE topics (two and a half modules each), and must be taught by both ECE and EEE faculty.
2. Mathematics subjects to be renamed as M-I, M-II, M-III and so on.
3. Subject “Digital Electronics” can be renamed as “Switching Theory and Logic Design”. And also “Switching Theory and Logic Design” by Zvi Kohavi and Niraj K Jha book have to be included as the main reference book.
4. Subject “Principles of Electrical Engineering” may be renamed as “Electrical Engineering” and be made available for Mechanical Engineering and Civil Engineering. It was recommended to include a subject related to electrical engineering – Electrical Technology or Network Theory.
5. Subject “Special Functions and Complex Analysis” has to be renamed as “Special Functions and Complex Variables”.
6. The syllabus for CO may be reduced and merged with OS, so that OS is not an open elective.
7. Subject title “Engineering Economics and Accountancy” is not appropriate and shall be renamed as “Principles of Economics and Accountancy” or “MEFA”.
8. “Analog Communication” and “Digital Communication” should be in consecutive semesters - semester IV and semester V. Accordingly, other subjects like CO and CS may be shuffled.
9. Laboratory courses using trainer kits (like AC, DC and MPMC) need not be in the same semester as the respective theory subjects.
10. “Cellular Mobile Communication” and “Computer Networks” should be made as core subjects.
11. Tutorial credits are not required for “Management Fundamentals”.
12. Three subjects from three different areas may be offered as Professional Electives.
13. The topic “Resonance” could be included along with “Two Port Networks” in either BEEE or PEE.
14. The word “review” should be omitted from the syllabus of all courses.
15. Credits for Project may be increased and credits for Professional Electives may be decreased. (Minor Project – 3, Major Project – 12, PE-V – 3)
16. “Satellite Communication” can be merged with either “Optical Communications” or “Radar Systems” or that subject may include in core.
17. “Data Compression” may be renamed as “Data Compression Techniques”.
18. “Digital System Design” and “Satellite Communications” may be introduced instead of “ASIC” and “CAD” respectively.


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19. The credits for Basic Sciences subjects, Humanities and Sciences subjects and Project-related courses should be altered according to the recommended credit weightages given by AICTE.

Finally, Dr. Kesavan Gopal, Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution. The meeting concluded at 1:30 p.m.



Dr. Kesavan Gopal

Chairman, B.Tech (ECE) - BOS

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