



# Malla Reddy Engineering College

(AUTONOMOUS)

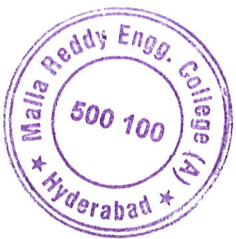
(An UGC Autonomous Institution Approved by AICTE and Affiliated to JNTU Hyderabad. Accredited by NAAC with 'A' Grade (II - Cycle))

## ORDER

Date: 10/06/2019

The ECE UG-Board of Studies for the Academic years 2019-20 and 2020-21 have been reconstituted as some of the previous members' relived from the institute, as per the UGC guidelines.

S. No.	Name of the Member	Designation & Official Address	Subject Specialization	Contact No.	Email ID	Category
1	Dr. Madhu Babu Sikha	Assistant Professor and HoD, Dept. of ECE, MREC (A)	Communication Systems	9042857114	sikha.madhu@gmail.com	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	Computer Networks	9441189228	dsraoece@jntuh.ac.in	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
3	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	VLSI & Image Processing	9441222226	rajendranaikb@osmania.ac.in	Subject Expert (outside the Parent University) Member
4	Dr. J Ravi Kumar	Professor, Dept. of ECE, NITW	Signal processing	8332969363	ravikumar@nitw.ac.in	Subject Expert (outside the Parent University) Member
5	Mr. K. Raghavendra	Senior Manager, A&T, HAL, Hyderabad.	Electronics	8919474132	kanikeraghu@yahoo.co.in	Industry Expert
6	Dr. T. Swapna	Professor	Embedded Systems	9948195593	swapnathouti@gmail.com	Faculty Member
7	Dr. T.Srinivas Reddy	Associate Professor	Image processing	9492571803	srimrec.2014@gmail.com	Faculty Member
8	Dr. Subbu Lakshmi	Associate Professor	VLSI	9843910092	Lakshu.125@gmail.com	Faculty Member
9	Ms. C. Silpa	Associate Professor	Communications	9490421721	srsilpavasu@gmail.com	Faculty Member
10	Mr J. Sunil Kumar	Assistant Professor	Systems and Signal Processing	7032677567	kmrsja@gmail.com	Faculty Member (Internal)
11	Mr. Bhalky Shiva kumar	Hardware Application Engineer, Unistring Tech Solutions	Embedded Systems	7396110499	shivakumarnkd@gmail.com	Alumni (MR16 Batch)



**Principal**  
Malla Reddy Engineering College  
(Autonomous)  
Maisammaguda, Dhulapally,  
(Post Via Kompally), Sec'bad-500 100

Maisammaguda, Dhulapally (Post Via Kompally), Secunderabad - 500 100.  
Phone: 8885542037, Fax: 040-23792153

Email: principal@mrec.ac.in  
www.mrec.ac.in



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Maisammaguda, Dhullapally (post via Kompally) Secunderabad-500100

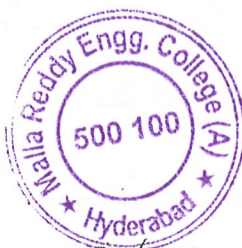
## Department of Electronics & Communication Engineering

Date: 21-06-2019

The Board of Studies meeting for the Department of Electronics & Communication Engineering is convened on 21.06.2019 at 10.00 AM in the Department conference hall, Mall Reddy Engineering College (Autonomous), Hyderabad. The Agenda of the meeting is as follows.

### AGENDA

1. Action Taken Report (ATR) on previous BoS Meeting in A.Y 2018-19
2. Discussion and review of B. Tech. (ECE) MR18 regulations course schema and syllabus.
3. Suggestion and review panel of names for Paper setters, and examiners for both theory and practical examinations that will be submit for Academic council for approval.
4. Suggestion and approval of open source softwares'.
5. Suggest methodologies for innovative teaching and evaluation techniques.
6. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.
7. Delegation of power to the Chairman-BOS based on recommendations of the internal committee, for the for inclusion and exclusion of any item as per requirements.
8. Approval of any other item with the permission of the Chairman.



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21/6/19

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21/6/2019

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BoS Chairman-ECE

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Principal  
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(Post Via Kompally), Sec'bad-500100

# MALLA REDDY ENGINEERING COLLEGE

(Autonomous)


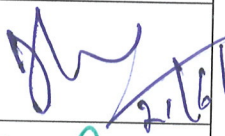
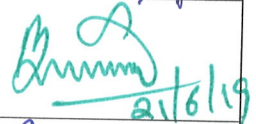
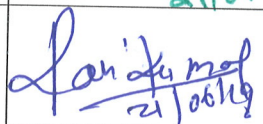



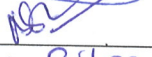

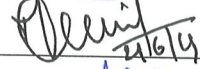

Maisammaguda, Dhullapally (post via Kompally) Secunderabad-500100

## Department of Electronics & Communication Engineering

Date: 21-06-2019

The Board of Studies meeting for the Department of Electronics & Communication Engineering is convened on 21.06.2019 at 10.00 AM in the Department conference hall, Mall Reddy Engineering College (Autonomous), Hyderabad.

The following members have attended to review on matters such as Schema, Syllabus of MR18 regulations (4 Year B.Tech (ECE) Course) and other points as per agenda.

S. No.	Name of the Member	Designation & Official Address	Category	Signature
1	Dr. Madhu Babu Sikha	Assistant Professor and HoD, Dept. of ECE, MREC (A)	Chairman- BOS	
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)	 21/6/19
3	Dr. B. Rajendra Naik	Professor, Dept. of ECE, OU Hyderabad.	Subject Expert (outside the Parent University) Member	 21/6/19
4	Dr. J Ravi Kumar	Asso Professor, Dept. of ECE, NITW	Subject Expert (outside the Parent University) Member	 21/06/19
5	Mr. K. Raghavendra	Senior Manager, A&T, HAL, Hyderabad.	Industry Expert	 ← A →
6	Dr. T. Swapna	Professor	Faculty Member	
7	Dr. T.Srinivas Reddy	Associate Professor	Faculty Member	
8	Dr. Subbu Lakshmi	Associate Professor	Faculty Member	
9	Ms. C. Silpa	Associate Professor	Faculty Member	 C-Silpa
10	Mr J. Sunil Kumar	Assistant Professor	Faculty Member (Internal)	 21/6/19
11	Mr. Bhalky Shiva kumar	Hardware Application Engineer, Unistring Tech Solutions	Alumni (MR16 Batch)	 B Shiva

# MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics and Communication Engineering

## Minutes of Meeting B. Tech (ECE) - BOS

June 21, 2019

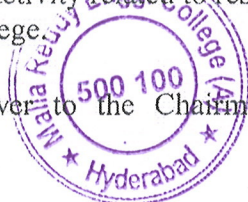
The 11<sup>th</sup> Board of Studies meeting for the B. Tech (ECE) - BOS was conducted on 21<sup>st</sup> June 2019 at 11:30 a.m. in the conference hall, Malla Reddy Engineering College (Autonomous), Hyderabad.

The following BOS members have attended the meeting:

1. Dr. S Sudhakara Reddy	Principal, MREC(A)	Special Invitee
2. Dr. S. Madhu Babu	Chairman-BOS, Assoc. Professor, ECED, MREC(A)	Chairman-BOS
3. Dr. D. Srinivasa Rao	Professor & Director, Academic & Planning, ECED, JNTUHCEH, Hyderabad	University Nominee
4. Dr. B. Rajendra Naik	Professor, ECED, OU, Hyderabad	Subject Expert
5. Dr. J. Ravi Kumar	Professor, ECED, NITW	Subject Expert
6. Mr. K. Raghavendra	Sr. Manager, A&T, HAL, Hyderabad	Industry Expert
7. Dr. T. Swapna	Professor, ECED, MREC(A)	Faculty Member
8. Dr. T Srinivasa Reddy	Assoc. Professor, ECED, MREC(A)	Faculty Member
9. Dr. N. Subbu Lakshmi	Assoc. Professor, ECED, MREC(A)	Faculty Member
10. Mrs. C Silpa	Assoc. Professor, ECED, MREC(A)	Faculty Member
11. Mr. J. Sunil Kumar	Asst. Professor, ECED, MREC(A)	Faculty Member
12. Mr. B. Shiva Kumar	Hardware Application Engineer. Unistring Tech Solutions	Alumni

The following was the agenda for the meeting.

1. Discussion and review of B. Tech (ECE) MR18 regulations course schema and syllabus.
2. Suggestion and review of panel for Paper setters, Valuers, and examiners for both theory and practical examinations that will be submit for Academic council for approval.
3. Suggestion and approval of open source softwares'
4. Suggestion and approval of list of additional topics beyond the syllabus approved in the BOS and Academic council, to be covered in the class room for both theory and practical courses, that can be taught in about 3 to 5 hours / semester / course.
5. Suggest methodologies for innovative teaching and evaluation techniques;
6. Suggestion of any activity related to research, teaching, extension and other academic activities in the department/college.
7. Delegation of power to the Chairman-BoS for inclusion and exclusion of any item as per requirements.



**Principal**  
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- Approval of any other item with the permission of the Chairman.

At the outset, Dr. S. Madhu Babu, Assoc. Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, welcomed the members to the Board of Studies meeting and appraised the purpose of the meeting. He also briefed about the previous BOS meeting, resolutions and placed on record, the existing syllabus of MR 18 regulations and also proposed modifications for MR19 regulations syllabus (Batches admitted from 2019-20 onwards). Later Board of studies members have in detail discussed, reviewed each item of the proposed agenda and finally the following items have been resolved unanimously by all the members of BOS for their implementation from the academic year 2019-20 and onwards.

**The following resolutions are made after careful discussion and observations.**

- The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for all 4 years of B. Tech (ECE) course as per the guide lines of the AICTE and JNTUH, Hyderabad. The BOS has collectively resolved to implement this scheme of Instructions, periods per week and number of credits for all 4 years of B. Tech (ECE) Course from the academic year 2017-18 onwards.
- After formulation of scheme of Instructions, the BOS has reviewed and devised the syllabus for B. Tech (ECE) Course as per the guide lines of the AICTE and JNTUH, Hyderabad.
- The BOS Members unanimously approved to use the following Open Source software for execution of programmes in the respective laboratories.

S. No.	Lab Name	Software Required
1	Analog Electronics	TINA /Multisim /Any Equivalent Open Source Software
2	Signals and Stochastic Process Lab	MATLAB/Octave/Any Equivalent open Source Software
3	Analog and Digital communications Lab	Octave/Any equivalent open source software
4	Micro Processors and Micro Controllers lab	MASM,KEIL & Flash Magic/ Any equivalent open source software
5	Digital Signal Processing Lab	MATLAB/Octave/Any Equivalent Open Source Software
6	VLSI Design Lab	Cadence/Xilinx/Any Equivalent Open Source Software
7	Digital Image Processing Lab	MATLAB/ Octave/ Any Equivalent Open Source Software
8	Computer Networks Lab	Packet tracer/NS-2& NS-3/ Wireshark/ Any Equivalent Open Source Software

- The BOS members have agreed to suggest a panel for paper setters and evaluators / examiners for both theory and practical of all the subjects of B. Tech (ECE) Course.



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Principal  
Maisamma Engineering College  
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5. The BOS has also resolved for delegation of power to the Chairman of BOS for inclusion / exclusion / modification of any of the above item as per the requirements.

The following suggestions were made by the members of the Board of studies.

1.

Finally, Dr. S. Madhu Babu, Assoc. Professor, ECE Department and Chairman of the B. Tech (ECE) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution. The meeting concluded at 1:30 p.m.

1. Committee has suggested Based on the content of the syllabus the title of the course (Mathematics-I & Mathematics-II) must be changed.
2. Committee has suggested Electronics Workshop Instead of Engineering Workshop.
3. Committee has instructed Tuned amplifier need to be included in Analog Electronics.
4. Scripting Languages subject has to include instead of Biomedical Instrumentation
5. Digital Image Processing Lab need to be done with Python and the Raspberry Boards.
6. Committee is suggested some of the professional electives to be replaced  
Software Defined Radio  
Network Security and Cryptography  
Low Power VLSI  
Optimization Techniques

*SMB*

Dr. S. Madhu Babu

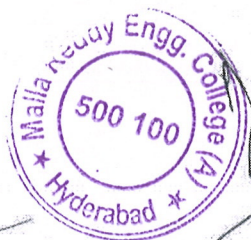
Chairman, B.Tech (ECE) - BOS

*Randhawa*  
21/6/17.

*A. S.*  
21/6/17.

*T. V. S.*  
21/6/17

*C. Suresh*



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21/6/17.

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*B. Suresh*

*R. Suresh*  
Principal

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# MALLA REDDY ENGINEERING COLLEGE (Autonomous)

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Department of Electronics & Communication Engineering

## Action Taken Report (ATR) on previous BoS Meeting held in AY 2018-19

Date: 08.12.2018

The previous Board of studies (UG) meeting for AY 2018-19 was held on dated 3<sup>rd</sup> July 2018. The following are the resolutions made in the meeting and corresponding actions taken:

1	<b>Resolution</b>	To include failures in each subject where remedial classes were taken up.
	<b>Action Taken</b>	The BOS chairman has presented the information about the failures in each subject where remedial classes were taken up.
2	<b>Resolution</b>	After formulation of scheme of Instructions, the BOS has reviewed and devised the syllabus for B.Tech. (ECE) Course and approved as per the guide lines of the UGC, AICTE and JNTUH, Hyderabad with 160 credits.
	<b>Action Taken</b>	The devised syllabus is implemented as MR18 regulation B.Tech. (ECE) course syllabus with 160 credits is implemented from the academic year 2018-19 onwards.
3	<b>Resolution</b>	The BOS has discussed and framed the syllabus of the B.Tech. subjects offered to the other Engineering branches namely B.Tech.(EEE), B.Tech.(CSE) as per the guide lines of the UGC, AICTE & JNTUH, Hyderabad and approved & resolved to implement from the academic year 2018-19 onwards.
	<b>Action Taken</b>	Syllabus of the B.Tech. Subjects offered to the other Engineering branches namely B.Tech. (EEE), B.Tech.(CSE) is implemented from the academic year 2018-19 onwards.
4	<b>Resolution</b>	The BOS Members unanimously approved to use the Open Source software for execution of programs in the respective laboratories.
	<b>Action Taken</b>	The approved Open Source software will be used for execution of programs in the respective laboratories devised in MR18 UG-B.Tech. (ECE).
5	<b>Resolution</b>	The BOS members have agreed to suggest a panel for paper setters and valuers /examiners for both theory and practical examinations of B.Tech. (ECE) Course.
	<b>Action Taken</b>	The panel of paper setters and valuers/examiners are implemented for both theory and practical of all the subjects of B.Tech. (ECE) Course.
6	<b>Resolution</b>	If any item needs to be modified /added/ deleted, the same may be circulated by E-mail to all members and after taking electronic/E-mail approval, the modification may be made operational.
	<b>Action Taken</b>	Implemented from the academic year 2018-19.
7	<b>Resolution</b>	Suggestions were made by the members of the Board of studies; to modify the titles Mathematics-I, II and III courses
	<b>Action Taken</b>	Implemented.
8	<b>Resolution</b>	Suggestions were made by the members of the Board of Studies to include ARM processors in the syllabus of MPMC
	<b>Action Taken</b>	ARM Processors are introduced in PE-IV: Advanced Microcontrollers course
9	<b>Resolution</b>	Suggestions were made by the members of the Board of studies; In professional Elective-V CPLD/FPGA to be replaced with SOC (System on Chip)

*Rawat*  
Principal  
Malla Reddy Engineering College  
Maisammaguda, Dhulapally,  
Post-Via Kompally, Sec'bad-500100.

	<b>Action Taken</b>	System on Chip course is introduced instead of CPLD/FPGA as PE-V.
10	<b>Resolution</b>	To prepare Student Innovation projects
	<b>Action Taken</b>	Principal has informed that Funding is provided for UG and PG Projects under college fund and submission of Innovation projects.
11	<b>Resolution</b>	Suggestions were made by the members of the Board of studies; IOT/Speech Processing/Network Security and Cryptography to be introduced in the electives.
	<b>Action Taken</b>	IoT and Applications course is introduced as PE-V.
12	<b>Resolution</b>	Suggestions were made by the members of the Board of Studies; Computer Networks course content to be modified.
	<b>Action Taken</b>	Data Communications and Computer Networks course and lab contents are modified according to the suggestions.

*Raush*

**Principal**  
**Jalla Reddy Engineering College**  
**Maisammaguda, Dhulapally,**  
**(Post Via Kompally), Sec'bad-500100,**

*for SMP*

**P.S.Indrani**  
**Chairman, ECE (UG) - BOS**

**Head, Department of ECE,**  
**Jalla Reddy Engineering College (A),**  
**Maisammaguda, Dhulapally,**  
**Secunderabad-500100, T.G.**