



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)
(UGC Autonomous Institution, Affiliated to JNTUH, Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)

Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100

Department of Electronics and Communication Engineering

CIRCULAR

Date: 14.02.2022

All the students are hereby informed that value added courses on **NI Lab VIEW CLAD** Course from 21/02/2022 to 25/02/2022 is being organized by the Department of Electronics and Communication Engineering. The resource person for this course is Mr. Vijaya Chander, Software Engineer, NI Lab View Academy National Instruments, Chennai. Students are advised to register their names to the programme coordinator on or before 19/02/2022 and utilize this opportunity to enhance their skills by attending the programme.

The detailed program will be displayed in notice board

Copy to

- 1) Circulation in students classroom
- 2) All HOD's
- 3) Notice board
- 4) PA to principal for filing


HOD, ECE
Head, Dept. of ECE
Malla Reddy Engg. College
Maisammaguda, Dhullapally,
Secunderbad-500100


PRINCIPAL
Principal
Malla Reddy Engineering College
Maisammaguda, Dhullapally,
(Post Via Kompally), Sec'bad-500100.

About the Institution

Malla Reddy Engineering College (Autonomous) is one of the reputed engineering colleges in Hyderabad, Telangana established in 2002. **MREC (A)** is part of Malla Reddy Group of Institutions (MRGI), founded by Sri. Ch. Malla Reddy Gaaru, currently Hon'ble Minister of Labor & Employment, Factories, Govt. of Telangana State. The college is situated in a serene, lushgreen environment in Maisammaguda, Gundlapochampally, Medchal (M), Medchal I- Malkajgiri District Telangana-500100.

MREC (A) is skillfully and smartly guided by **Dr. A. Ramaswami Reddy, Principal, MREC (A)** which is affiliated to JNTUH and an autonomous institution approved by UGC & JNTU Hyderabad first in Telangana state in 2014 for a period of 6 years from 2014-2020. The college is re-accredited by NAAC with 'A' Grade (II Cycle). Our eligible UG and PG programs received NBA accreditation twice. The college caters to wide ranging aspirations and goals of student communities by offering regular programs in various streams of Engineering and Management.

It provides world-class infrastructure facilities like RnD, Incubation Centre, EDC, IIIC, IPR, Start ups, Centre of Excellence, Innovation Council, many clubs for Faculty & Students and well-equipped laboratories in all departments

MREC (A) has more than 100 doctorates with good Research work.

Advisory Committee:

Chief Patrons: **Sri. Ch. Malla Reddy, Minister-Telangana State-India.**
Founder Chairman,
Malla Reddy Group of Institutions.

Patrons: **Sri.Ch. Mahender Reddy.**
Secretary, MRGI.
Dr.Ch.Bhadra Reddy,
President, MRGI.

Co-Patrons: **Dr. A. Ramaswami Reddy**
Principal, MREC (A).

Convener: **M. Jagdeesh Chandra Prasad**
HOD ECE.

Co-ordinator: **Dr. M. Nithin Varma,**
Assoc. Professor, ECE.

Organizing Committee:

Dr.G.S.K.Gayathri Devi, Assoc. Prof., ECE.
Mr G Prasanna Kumar Assoc . Prof., ECE.
Dr. M.Nithin Varma Asst.Prof.,ECE
Mrs.C.Silpa, Assoc. Prof., ECE.
Dr A Pradeep Kumar,, Assc. Prof., ECE
Dr T Srinivas Reddy, Assoc. Prof., ECE.
Mr. J. Sunil Kumar,Asst.Prof.,ECE
Mr.Vutkuri Srinivas, Asst.Prof.,ECE
Mr. M.Kranthi Kumar, Asst.Prof.,ECE
Mrs. G.Jyothi,Asst.Prof.,ECE

5-days Value Added Course
on

" NI Lab VIEW CLAD "

From 21th to 25th February 2022



Organized by

Department of

Electronics and Communication Engineering

MALLA REDDY ENGINEERING COLLEGE

(AUTONOMOUS) MAIN CAMPUS

An UGC Autonomous Institution, Approved by

AICTE & Affiliated to JNTUH-Hyderabad

Reaccredited by NAAC with 'A' Grade (II Cycle)

Maisammaguda(H), Gundlapochampally (V),

Medchal (M), Medchal - Malkajgiri District

Telangana - 500100, India.

About the Department:

The Department of Electronics and Communication Engineering at MREC has been producing high quality technical manpower needed by industry, R&D organizations, and academic institutions since 2002 with an Intake of 60. The intake has been increased to 120 in the year 2004-05 and 180 in the year 2013-14. The Department also offering PG Program **VLSI and Embedded Systems**. The department UG Program was accredited by NBA in 2008 and Re-accredited in 2014 and 2018.

Electronics and Communication Engineering department is now headed by **Dr. M. Jagdeesh Chandra Prasad** Department maintaining 1:15 student faculty ratio with enough faculty retention. Total 30% of doctorates available in the department. Faculty Publications are 150, Patents-5, sanctioned UGC Minor Project, AICTE (AQIS) sponsored MODROB & FDP and Consultancy projects-4. Applied projects for research grants under AQIS, CRG.

Many of our students are placed in reputed companies like INFOSYS, IBM, AMAZON, Tech-Mahindra, TCS, COGNIZANT and many more. Apart from teaching department also provides technical guidelines for the benefits of students by organizing short-term courses, certificate oriented training, seminars, Guest lectures, workshops, national and international conferences.

About Workshop

PROGRAMME OBJECTIVE : The main objective of this workshop is to equip the students with the fundamental concepts and tools of Lab VIEW design. This course offers a wide range of topics that are of immediate relevance to the industry and train the students in Lab VIEW development for Real Time applications. Lab VIEW is a graphical programming language used for simulation as well as building real-world systems in association with NI modules.

PROGRAMME CONTENTS

The following topics will be covered

Introduction to Lab VIEW

Customize Lab VIEW and Graphical Programming

Lab VIEW – Sub VI Debugging tools in Lab VIEW

Lab VIEW programming using Structures, Arrays and Clusters Hands-on Training using NI ELVIS III User interface & error handling

Hands-on Training using my DAQ Hands-on Training using Analog Discovery Kit 2 Lab VIEW based projects

RESOURCE PERSONS Certified faculties from Institutions/ Industrial experts will deliver the lectures and handle practical sessions from National Instruments

Outcome of the program

After completing the Course, Student will learn how to do simulation and Design with LABVIEW

Registration Form

5-days Value Added Course

on

" NI Lab VIEW CLAD "

From 21th to 25th February 2022

Name: _____

Roll Number _____

Department: _____

Organization: _____

Address: _____

Mobile No: _____

E-mail ID: _____

Signature of the Candidate: _____

OR


Interested students are can register using below link

<https://forms.gle/M91WTujRUdPknyPq5>

Note: Certificate will be provided for the participants on feedback submission, 80% attendance.

Registration Fee :

1000 Rs registration fee will be charged for attending this programme.

| | |
|---|--|
|  | <p align="center">MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS) (UGC Autonomous Institution, Affiliated to JNTUH, Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1) Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100</p> |
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course on “NI Lab VIEW CLAD”

21th -25th February 2022
Programme Schedule

| Date | Time | Topic | Resource Person |
|------------|----------------|------------------------------------|-------------------|
| 21.02.2022 | 9:30 – 4.00 pm | Introduction to Lab VIEW | Mr.Vijaya Chander |
| 22.02.2022 | 9:30 – 4.00 pm | Application of DAQ using Lab VIEW | Mr.Vijaya Chander |
| 23.02.2022 | 9:30 – 4.00 pm | Lab VIEW with DAQ-USB-6009 | Mr.Vijaya Chander |
| 24.02.2022 | 9:30 – 4.00 pm | PLC interface with Lab VIEW | Mr.Vijaya Chander |
| 25.02.2022 | 9:30 – 4.00 pm | Hardware interfacing with Lab VIEW | Mr.Vijaya Chander |


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value Added Course on “NI Lab VIEW CLAD”

21th -25th February 2022

Syllabus

Day 1

- Introduction to Lab VIEW
- Customize Lab VIEW and Graphical Programming

Day 2

- Hands-on Training using my DAQ
- Hands-on Training using Analog Discovery
- Kit 2 Lab VIEW based projects
- Hands-on Training using my DAQ
- Hands-on Training using Analog Discovery
- Kit 2 Lab VIEW based projects

Day 3


- Final Module test : Lab VIEW with DAQ-USB-6009
- Embedded device with lab VIEW

Day 4


- PLC interface with Lab VIEW
- Programming application web server

Day 5

- Hardware interfacing with Lab VIEW
- Final Lab VIEW Project
- Hardware interfacing with Lab VIEW
- Final Lab VIEW Project


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| | | Course: B.Tech |
| | | Date: 21.02.2022 to 25.02.2022 |

Department of Electronics and Communication Engineering

Academic Year 2021-2022

Name of the Value Added Course: NI Lab VIEW CLAD


List of Students enrolled for value added course

| Sl. No. | Roll No. | Name | Year/Semester |
|----------------|-----------------|---|----------------------|
| 1 | 18J41A0401 | ARIGELA SRINIVAS | IV/II |
| 2 | 18J41A0402 | ARUTLA ABHINAV REDDY | IV/II |
| 3 | 18J41A0403 | AVULA NIKITHA | IV/II |
| 4 | 18J41A0404 | B PAVAN KUMAR | IV/II |
| 5 | 18J41A0405 | B SRAVANTHI | IV/II |
| 6 | 18J41A0406 | BAPANAPALLI V V NAGA SATYA SAI DATTA | IV/II |
| 7 | 18J41A0407 | BOBBILI DHARANI | IV/II |
| 8 | 18J41A0408 | BYRISHETTY KEERTHANA | IV/II |
| 9 | 18J41A0409 | CHILUKURI ISHITHA | IV/II |
| 10 | 18J41A0410 | CHINTHAKUNTALA ADARSH REDDY | IV/II |
| 11 | 18J41A0411 | DANDAMUDI SAI VARDHAN | IV/II |
| 12 | 18J41A0412 | DASARI SAINATH REDDY | IV/II |
| 13 | 18J41A0413 | DONTULA GOPALA KRISHNA | IV/II |
| 14 | 18J41A0414 | DURGAPU VENNELA | IV/II |
| 15 | 18J41A0416 | GADIGOPPULA POOJITHA | IV/II |
| 16 | 18J41A0417 | GUNDU DIVYA | IV/II |
| 17 | 18J41A0418 | KACHALA SRI VENKATA SAI GREESHMA | IV/II |
| 18 | 18J41A0419 | KARIYADA HARSHAVARDHAN REDDY | IV/II |
| 19 | 18J41A0420 | KECHELA PRAVEEN KUMAR REDDY | IV/II |
| 20 | 18J41A0421 | KESI REDDY MOUNIKA REDDY | IV/II |
| 21 | 18J41A0422 | KOLAGOTLA RAHUL REDDY | IV/II |

| | | | |
|----|------------|------------------------------|-------|
| 22 | 18J41A0423 | KOLLAM NITHIN MOHAN | IV/II |
| 23 | 18J41A0424 | KONKALA KRISHNA SREEKAR | IV/II |
| 24 | 18J41A0425 | KOTRA PRAVALLIKA | IV/II |
| 25 | 18J41A0426 | KUNJA SANTHOSH | IV/II |
| 26 | 18J41A0427 | KURAPATI KAVYASREE | IV/II |
| 27 | 18J41A0428 | LAKSHMI CHENNA KESHAVA ANDE | IV/II |
| 28 | 18J41A0429 | MANNE USHA SRI | IV/II |
| 29 | 18J41A0461 | A KRANTHI | IV/II |
| 30 | 18J41A0462 | ADICHERLA HANEETH | IV/II |
| 31 | 18J41A0463 | AJMEERA ANIL KUMAR | IV/II |
| 32 | 18J41A0464 | ALAKUNTALA SAI PRASHANTH | IV/II |
| 33 | 18J41A0465 | ALASYAM CHANDANA | IV/II |
| 34 | 18J41A0466 | ALLURI AMULYA | IV/II |
| 35 | 18J41A0467 | AVVARU YUVA NAGA TEJA | IV/II |
| 36 | 18J41A0468 | B NANDINI | IV/II |
| 37 | 18J41A0469 | BETHEL SOLOMON | IV/II |
| 38 | 18J41A0470 | BODDIREDDY HRUTHIK REDDY | IV/II |
| 39 | 18J41A0471 | BOMMIREDDY LOKESH REDDY | IV/II |
| 40 | 18J41A0472 | BUDDHARAJU SRI SINGDHA PRIYA | IV/II |
| 41 | 18J41A0473 | CHANDA SAI PRAKASH | IV/II |
| 42 | 18J41A0474 | CHIDIRALA SARAN SHANKAR | IV/II |
| 43 | 18J41A0475 | CHINTHAMALLA VAMSHI | IV/II |
| 44 | 18J41A0476 | DAMA GANDHI | IV/II |
| 45 | 18J41A0477 | DODDOLLA NAVEEN KUMAR | IV/II |
| 46 | 18J41A0478 | DOKKU NAVYA SRI | IV/II |
| 47 | 18J41A0479 | DURGAM AJAY | IV/II |
| 48 | 18J41A0480 | DUSA JAYA KRISHNA | IV/II |
| 49 | 18J41A0461 | A KRANTHI | IV/II |
| 50 | 18J41A0470 | BODDIREDDY HRUTHIK REDDY | IV/II |
| 51 | 18J41A0471 | BOMMIREDDY LOKESH REDDY | IV/II |
| 52 | 18J41A0472 | BUDDHARAJU SRI SINGDHA PRIYA | IV/II |
| 53 | 18J41A0473 | CHANDA SAI PRAKASH | IV/II |
| 54 | 18J41A0474 | CHIDIRALA SARAN SHANKAR | IV/II |
| 55 | 18J41A0475 | CHINTHAMALLA VAMSHI | IV/II |
| 56 | 18J41A0476 | DAMA GANDHI | IV/II |
| 57 | 18J41A0477 | DODDOLLA NAVEEN KUMAR | IV/II |
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| 59 | 18J41A0479 | DURGAM AJAY | IV/II |
| 60 | 18J41A0480 | DUSA JAYA KRISHNA | IV/II |

| | | | |
|----|------------|-------------------------|-------|
| 61 | 18J41A0481 | DWIVEDULA SAHITI | IV/II |
| 62 | 18J41A0482 | GADASANTHALA PRATYUSHA | IV/II |
| 63 | 18J41A0483 | GADDAM HARISH KUMAR | IV/II |
| 64 | 18J41A0484 | GAMPA SAI TEJA | IV/II |
| 65 | 18J41A0485 | GANDHARI NAVATEJA REDDY | IV/II |
| 66 | 18J41A0486 | GOURU HARI PRASAD | IV/II |
| 67 | 18J41A0487 | GUMPU VIJAYAKUMARI | IV/II |
| 68 | 18J41A0488 | GUNDALA SRAVYA | IV/II |
| 69 | 18J41A0489 | GURRAMKONDA SUNEETHA | IV/II |


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

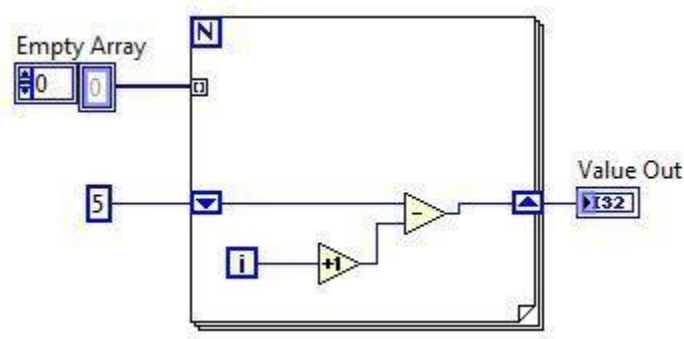
Name of the Value Added Course: NI Lab VIEW CLAD

Date: 21/02/ 2022

Objective Questions

- For implementing state diagrams that allow future application scalability, the best choice for a base structure is?
 - Sequence structure
 - Case structure
 - Formula node
 - Object-Oriented structure
- Clicking on the _____ button allows you to bypass a node in the Block Diagram without single-stepping through the node.
 - Step Into
 - Step Over
 - Step Out
 - Step Through
- Which of the following apply to Property Nodes? **(More than one answer may apply.)**
 - Property Nodes allow attributes of front panel objects to be programmatically manipulated.
 - Property Nodes can be used to update the values contained in a front panel object.
 - More than one Property Node can be used for a single front panel object.
 - Property Nodes can be used to programmatically generate a Value Change event.
- You have a front panel control on a top-level VI that you must control from within a subVI. What must you pass to the subVI?
 - The control's properties
 - The control's methods
 - The control's reference
 - The control's data type

5. What value does the **Value Out** indicator display after the VI executes?



- a. 0
- b. 4
- c. 5
- d. 6

6. You set a Breakpoint in the code contained within the False case of a Case structure. Will the VI execution pause if the code in True case of the Case structure is being executed?

- a. Yes
- b. No

7. Which Property Node changes the text color attributes of any digital numeric control from a subVI?

- a.

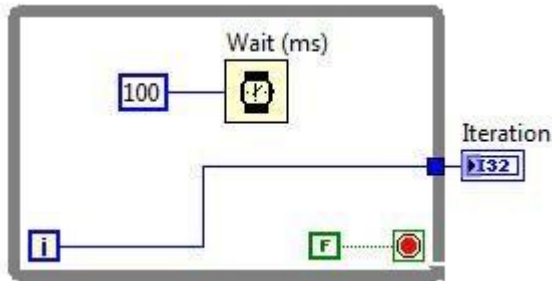
| |
|-------------------|
| DigNum (strict) |
| NumText.BGColor |
| NumText.TextColor |
- b.

| |
|-------------------|
| DigNum |
| NumText.BGColor |
| NumText.TextColor |
- c.

| |
|-------------------|
| DigNum |
| NumText.BGColor |
| NumText.TextColor |
- d.

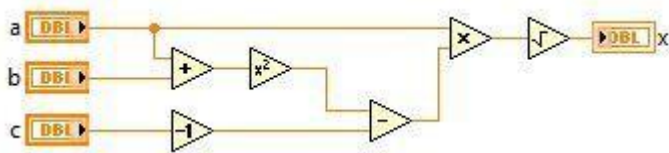
| |
|---------------------|
| Num |
| UnitLabel.BGColor |
| UnitLabel.TextColor |

8. Which chart update mode should be used to show running data continuously scrolling from left to right across the chart?
- a. Strip Chart
 - b. Scope Chart
 - c. Sweep Chart
 - d. Step Chart
9. Which of the following does not conform to the Dataflow programming paradigm?
- a. Shift Registers
 - b. Tunnels
 - c. SubVIs
 - d. Local variables
10. If possible, a Sequence structure should be replaced with a(n):
- a. Event structure
 - b. For loop
 - c. State machine
 - d. While loop
11. Which of the following statements is **TRUE** about the following code segment?



- a. The loop will execute once and the indicator Iteration will have a value of one
- b. The loop will execute once and the indicator Iteration will have a value of zero
- c. The loop will execute infinitely and the program will have to be aborted
- d. The loop will not execute and the indicator Iteration will have a value of zero

12. Which equation is equivalent to the code?



a.
$$x = \sqrt{a \times ((a + b)^2 - (c - 1))}$$

a.

b.
$$x = \sqrt{a \times ((c - 1) - (a + b)^2)}$$

b.

c.
$$x = \sqrt{a \times ((a + b^2) - (c - 1))}$$

c.

d.
$$x = \sqrt{a^2 \times ((a + b)^2 - (c - 1))}$$

d.

13. A VI has Automatic Error Handling enabled. It calls a SubVI that has Automatic Error Handling disabled. The Error Out terminal of the SubVI is unwired in the calling VI. The SubVI returns an error to the calling VI. Will an error dialog be posted because of Automatic Error Handling?

- a. No, since the SubVI has Automatic Error Handling disabled
- b. Yes, since the SubVI has Automatic Error Handling disabled
- c. No, since the Error Out terminal from the subVI is not wired in the calling VI.
- d. Yes, since the Error Out terminal from the subVI is not wired in the calling VI.

14. Which of the following statements is **FALSE**?

- a. A SubVI connector pane defines where to wire inputs and outputs
- b. The color of a SubVI connector pane terminal matches the data type it is connected to
- c. You must have an icon/connector pane to use a SubVI
- d. A SubVI icon can be edited from the functions palette

15. Which of the following cannot be used to transfer data?

- a. Semaphores
- b. Queues
- c. Notifiers
- d. Local variables

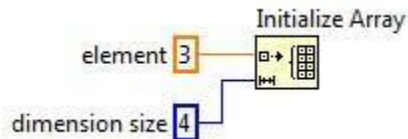
16. The Error list shows all of the following **EXCEPT**:

- a. Items with errors
- b. Errors and warnings
- c. Details about the warnings
- d. Error Codes

17. Auto-indexing at loop boundary is a feature that allows: **(More than one answer may apply.)**

- a. Arrays to be automatically traversed from one iteration to the next
- b. Array functions to automatically index array elements
- c. Functions that use arrays to automatically index them
- d. Arrays to be automatically accumulated at loop boundaries

18. What is the output of the Initialize Array function after the following code has executed?



- a. 1-D Array of {3, 3, 3, 3}
- b. 1-D Array of {4, 4, 4}
- c. 1-D Array of {3, 4}
- d. 1-D Array of {4, 3}

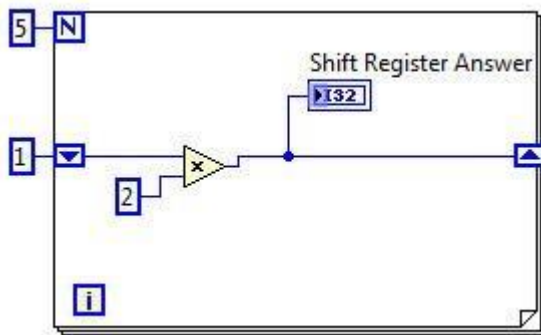
19. Which of the following will cause an event to be captured by the LabVIEW Event Structure?

- a. Changing a Front Panel control's Value via a mouse click on the control
- b. Changing a Front Panel control's Value property via a property node
- c. Changing a Front Panel control's Value via a control reference
- d. Changing a Front Panel control's Value via a local variable

20. Which variable is commonly used to eliminate race conditions by preventing simultaneous access to code or data?

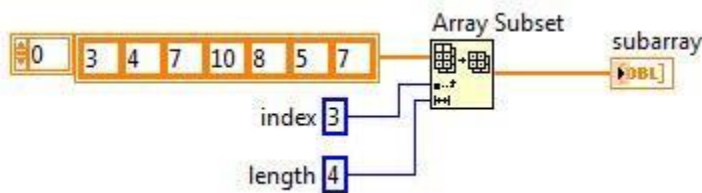
- a. Functional global variable
- b. Local variable
- c. Global variable
- d. Shared variable

21. What is the value in **Shift Register Answer** after the following code has executed?



- a. 16
- b. 24
- c. 32
- d. 10

22. What is the result in **subarray** after the following code has executed?



- a. 1-D Array of {8, 5, 7}
- b. 1-D Array of {10, 8, 5}
- c. 1-D Array of {10, 8, 5, 7}
- d. 1-D Array of {7, 10, 8, 5}

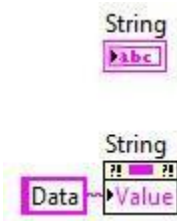
23. How do you document a VI so that the description appears in the Show Context Help popup window?

- a. Use the VI Properties Documentation window
- b. Type in the Show Context Help window
- c. Create a free label on the front panel
- d. Edit the LabVIEW help files

24. Which of the following terminals controls how many times a For loop executes?

- a.
- b.
- c.
- d.

25. Does this code snippet demonstrate an appropriate use of a Property Node?



- a. Yes, because updating the value via a Property Node uses less memory than writing to the terminal
- b. Yes, because updating the value via a Property Node allows for error checking
- c. No, because you should always wire data directly into indicators when possible
- d. No, because Property Nodes should only be used with more complex data types

26. The Wait Until Next ms Multiple function:

- a. Begins timing at program start and completes upon a multiple of the PC timer
- b. Begins timing after the code in the loop has completed and expires when the multiple of the (user) input time expires
- c. Begins timing after the code in the loop has completed and expires when the (user) input time completes
- d. Begins timing at program start and waits until the code in the loop has completed

27. Which data synchronization mechanism ensures that no data is lost when an application temporarily provides data faster than it is able to process it?

- a. Notifier
- b. Queue
- c. Semaphore
- d. Local Variable

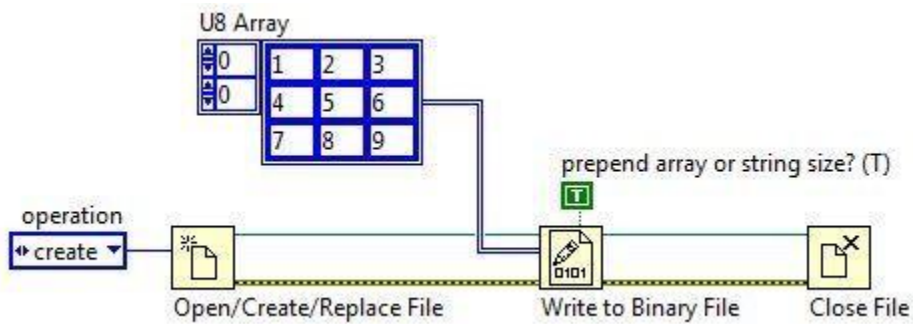
28. Which of the following statements is NOT valid:

- a. You can make a Cluster of Clusters.
- b. You can make an Array of Arrays.
- c. You can make a Cluster of Arrays.
- d. You can make an Array of Clusters.

29. Which data type is not accepted by the case selector terminal on a case structure?

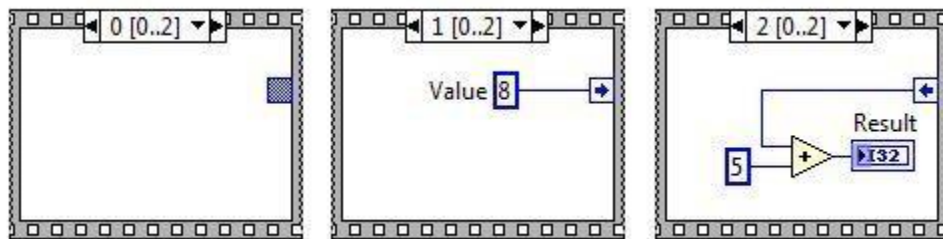
- a. Arrays
- b. Enumerated type values
- c. Strings
- d. Integers

30. How many bytes does LabVIEW write to the file when the code executes?



- a. 9 Bytes
- b. 11 Bytes
- c. 13 Bytes
- d. 17 Bytes

31. Why is the sequence local terminal displayed as unassigned in Frame 0 of the stacked sequence structure?



- a. The developer chose not to wire the value to any terminal in this frame
- b. The value is available only to frames after frame 1
- c. The data type of the terminal is incompatible with the data type of Value
- d. The developer disabled the terminal

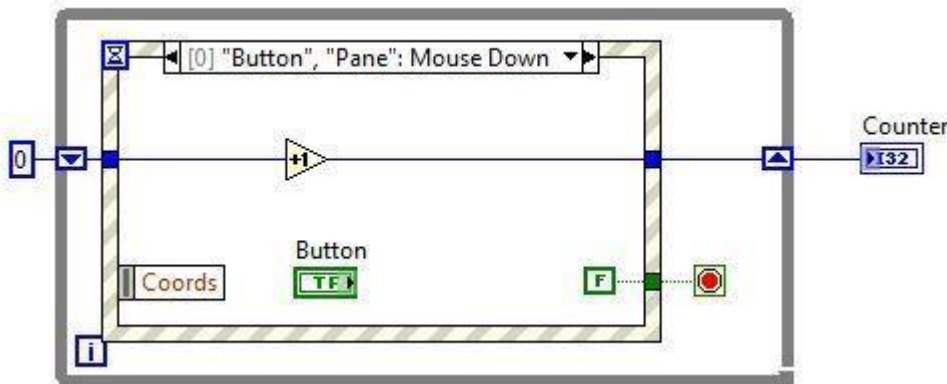
32. The Wait function can be added to While Loops:

- a. To free up available memory.
- b. To allocate memory used by the CPU.
- c. To allow the processor time to complete other tasks.
- d. To reserve which processor the code is running on.

33. The data type of a Ring can be any of the allowable data types, whereas the numerical representation of the data type of an Enum can only be of unsigned data type.

- a. True
- b. False

34. When the user clicks the **Button** control, how many times is the **Increment** function called?

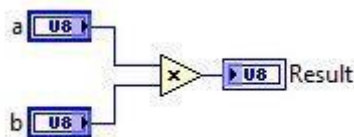


- a. 0
- b. 1
- c. 2
- d. 3

35. Why is LabVIEW a data flow programming language?

- a. It uses icons instead of text to create applications
- b. The flow of data through nodes on the block diagram determines execution
- c. It does not have a Goto function to control execution
- d. The memory location in which data is stored changes each iteration

36. If the value in **a** is 2 and **b** is 128, what value does the **Result** indicator display after the VI executes?



- a. -128
- b. 0
- c. 255
- d. 256

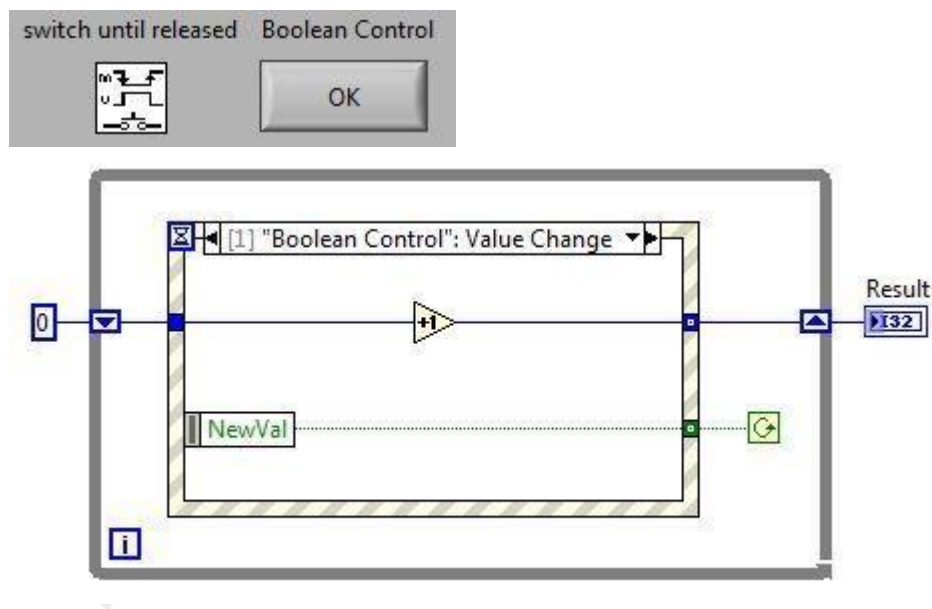
37. What VI is typically used to terminate an Error Cluster wire and to display any error message?

- a. Merge Errors
- b. One Button Dialog/Two Button Dialog
- c. Generate Front Panel Activity
- d. Simple Error Handler

38. Which of the following methods will create an XY Graph with two plots?

- a. Create a single cluster from two arrays of X values and two arrays of Y values in an X,Y, X,Y pattern.
- b. Bundle the X and Y array pairs together into two clusters and then create an array of these two clusters.
- c. Bundle the X and Y array pairs together into two clusters and create a cluster of these two clusters.
- d. Create a single array of each of the X arrays and Y arrays in an X,Y,X,Y pattern.

39. While the VI executes, the user presses and then releases **Boolean Control** with Switch Until Released mechanical action. Assuming that the starting value of **Boolean Control** is FALSE, what value does the **Result** indicator display after execution?




- a. 0
- b. 1
- c. 2
- d. 3

40. Which of the following is the best method to update an indicator on the front panel?


- a. Use a Value property node
- b. Wire directly to the indicator terminal
- c. Use a local variable
- d. Use a functional global variable



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| | | Course: B.Tech |
| | | Date: 21.02.2022 to 25.02.2022 |

Department of Electronics and Communication Engineering

Academic Year 2021-2022

Name of the Value Added Course: **NI Lab VIEW CLAD**

List of Students completed course

| Sl. No. | Roll No. | Name | Course Completion (Yes/No) |
|---------|------------|--------------------------------------|----------------------------|
| 1 | 18J41A0401 | ARIGELA SRINIVAS | Yes |
| 2 | 18J41A0402 | ARUTLA ABHINAV REDDY | Yes |
| 3 | 18J41A0403 | AVULA NIKITHA | No |
| 4 | 18J41A0404 | B PAVAN KUMAR | Yes |
| 5 | 18J41A0405 | B SRAVANTHI | Yes |
| 6 | 18J41A0406 | BAPANAPALLI V V NAGA SATYA SAI DATTA | No |
| 7 | 18J41A0407 | BOBBILI DHARANI | Yes |
| 8 | 18J41A0408 | BYRISHETTY KEERTHANA | Yes |
| 9 | 18J41A0409 | CHILUKURI ISHITHA | Yes |
| 10 | 18J41A0410 | CHINTHAKUNTLA ADARSH REDDY | Yes |
| 11 | 18J41A0411 | DANDAMUDI SAI VARDHAN | Yes |
| 12 | 18J41A0412 | DASARI SAINATH REDDY | Yes |
| 13 | 18J41A0413 | DONTULA GOPALA KRISHNA | Yes |
| 14 | 18J41A0414 | DURGAPU VENNELA | Yes |
| 15 | 18J41A0416 | GADIGOPPULA POOJITHA | Yes |
| 16 | 18J41A0417 | GUNDU DIVYA | Yes |
| 17 | 18J41A0418 | KACHALA SRI VENKATA SAI GREESHMA | Yes |
| 18 | 18J41A0419 | KARIYADA HARSHAVARDHAN REDDY | Yes |
| 19 | 18J41A0420 | KECHELA PRAVEEN KUMAR REDDY | Yes |
| 20 | 18J41A0421 | KESI REDDY MOUNIKA REDDY | Yes |

| | | | |
|----|------------|------------------------------|-----|
| 21 | 18J41A0422 | KOLAGOTLA RAHUL REDDY | Yes |
| 22 | 18J41A0423 | KOLLAM NITHIN MOHAN | Yes |
| 23 | 18J41A0424 | KONKALA KRISHNA SREEKAR | Yes |
| 24 | 18J41A0425 | KOTRA PRAVALLIKA | No |
| 25 | 18J41A0426 | KUNJA SANTHOSH | Yes |
| 26 | 18J41A0427 | KURAPATI KAVYASREE | Yes |
| 27 | 18J41A0428 | LAKSHMI CHENNA KESHAVA ANDE | Yes |
| 28 | 18J41A0429 | MANNE USHA SRI | Yes |
| 29 | 18J41A0461 | A KRANTHI | Yes |
| 30 | 18J41A0462 | ADICHERLA HANEETH | Yes |
| 31 | 18J41A0463 | AJMEERA ANIL KUMAR | Yes |
| 32 | 18J41A0464 | ALAKUNTALA SAI PRASHANTH | No |
| 33 | 18J41A0465 | ALASYAM CHANDANA | Yes |
| 34 | 18J41A0466 | ALLURI AMULYA | Yes |
| 35 | 18J41A0467 | AVVARU YUVA NAGA TEJA | Yes |
| 36 | 18J41A0468 | B NANDINI | Yes |
| 37 | 18J41A0469 | BETHEL SOLOMON | Yes |
| 38 | 18J41A0470 | BODDIREDDY HRUTHIK REDDY | Yes |
| 39 | 18J41A0471 | BOMMIREDDY LOKESH REDDY | Yes |
| 40 | 18J41A0472 | BUDDHARAJU SRI SINGDHA PRIYA | Yes |
| 41 | 18J41A0473 | CHANDA SAI PRAKASH | Yes |
| 42 | 18J41A0474 | CHIDIRALA SARAN SHANKAR | Yes |
| 43 | 18J41A0475 | CHINTHAMALLA VAMSHI | Yes |
| 44 | 18J41A0476 | DAMA GANDHI | Yes |
| 45 | 18J41A0477 | DODDOLLA NAVEEN KUMAR | Yes |
| 46 | 18J41A0478 | DOKKU NAVYA SRI | Yes |
| 47 | 18J41A0479 | DURGAM AJAY | Yes |
| 48 | 18J41A0480 | DUSA JAYA KRISHNA | Yes |
| 49 | 18J41A0461 | A KRANTHI | Yes |
| 50 | 18J41A0470 | BODDIREDDY HRUTHIK REDDY | Yes |
| 51 | 18J41A0471 | BOMMIREDDY LOKESH REDDY | Yes |
| 52 | 18J41A0472 | BUDDHARAJU SRI SINGDHA PRIYA | Yes |
| 53 | 18J41A0473 | CHANDA SAI PRAKASH | Yes |
| 54 | 18J41A0474 | CHIDIRALA SARAN SHANKAR | Yes |
| 55 | 18J41A0475 | CHINTHAMALLA VAMSHI | Yes |
| 56 | 18J41A0476 | DAMA GANDHI | No |
| 57 | 18J41A0477 | DODDOLLA NAVEEN KUMAR | Yes |
| 58 | 18J41A0478 | DOKKU NAVYA SRI | Yes |
| 59 | 18J41A0479 | DURGAM AJAY | Yes |

| | | | |
|----|------------|-------------------------|-----|
| 60 | 18J41A0480 | DUSA JAYA KRISHNA | Yes |
| 61 | 18J41A0481 | DWIVEDULA SAHITI | Yes |
| 62 | 18J41A0482 | GADASANTHALA PRATYUSHA | No |
| 63 | 18J41A0483 | GADDAM HARISH KUMAR | Yes |
| 64 | 18J41A0484 | GAMPA SAI TEJA | Yes |
| 65 | 18J41A0485 | GANDHARI NAVATEJA REDDY | Yes |
| 66 | 18J41A0486 | GOURU HARI PRASAD | Yes |
| 67 | 18J41A0487 | GUMPU VIJAYAKUMARI | Yes |
| 68 | 18J41A0488 | GUNDALA SRAVYA | Yes |
| 69 | 18J41A0489 | GURRAMKONDA SUNEETHA | No |


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Department of Electronics and Communication Engineering

Date: 21.02.2022

Report for value added course
on

NI Lab VIEW CLAD

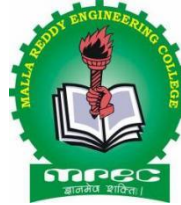
A Five days NI Lab VIEW CLAD programme was organized by the Department of Communication Engineering, and the event was coordinated by Dr. M. Nithin Varma, Associate Professor, ECE Department from 21 Feb. 2022 to 25 Feb. 2022 and 69 students were attended from IV year. The resource person for this course is Mr. Vijaya Chander. The quality of life for the students are enhanced by introspect and learn techniques that imbibe ethics and moral to help the students for active and successful participation in a modern society, producing individuals of high character, probity, and honor.

After the training programme, the students learned the following:

- Learned the use of DAQ card through hands on training
- Gained knowledge on PLC interface including • Programming application web server
- Acquired concepts on project development techniques using Lab VIEW


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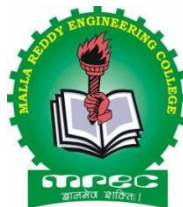
This is to certify that Mr./Ms. BAPANAPALLI V NAGA SATYA SAI DATTA bearing roll no. 18J41A0406 has successfully completed Value Added Course in “NI Lab VIEW CLAD” organized by Department of Electronics and Communication Engineering from 21/02/2022 to 25/02/2022.


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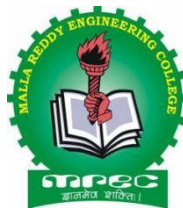
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This is to certify that Mr./Ms. LAKSHMI CHENNA KESHA VA ANDE bearing roll no. 18J41A0428 has successfully completed Value Added Course in “NI Lab VIEW CLAD” organized by Department of Electronics and Communication Engineering from 21/02/2022 to 25/02/2022.


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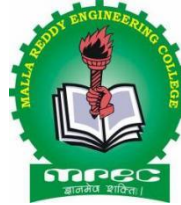
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This is to certify that Mr./Ms. **BUDDHARAJU SRI SINGDHA PRIYA** bearing roll no. **18J41A0472** has successfully completed Value Added Course in “NI Lab VIEW CLAD” organized by Department of Electronics and Communication Engineering from 21/02/2022 to 25/02/2022.


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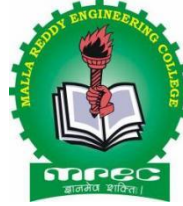
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This is to certify that Mr./Ms. CHIDIRALA SARAN SHANKAR bearing roll no. 18J41A0474 has successfully completed Value Added Course in “NI Lab VIEW CLAD” organized by Department of Electronics and Communication Engineering from 21/02/2022 to 25/02/2022


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This is to certify that Mr./Ms. GANDHARI NAVATEJA REDDY bearing roll no. 18J41A0485 has successfully completed Value Added Course in “NI Lab VIEW CLAD” organized by Department of Electronics and Communication Engineering from 21/02/2022 to 25/02/2022.


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Department of Electronics and Communication Engineering

Title of the Session: *NI Lab View clod.*

Department: *ECE*

Date of the Session: *21/2/2022 — 25/02/2022.*

| Sl No. | | Excellent | Very Good | Good | Average |
|--------|---|-----------|-----------|------|---------|
| 1 | The session objectives were stated clearly and met | | ✓ | | |
| 2 | The Workshop was well organized | | ✓ | | |
| 3 | The information and/or skills presented were relevant and useful for career | | ✓ | | |
| 4 | The presenter responded to questions effectively? | ✓ | | | |
| 5 | The overall assessment of the Session | | | ✓ | |
| 6 | The session content met your expectation | | | ✓ | |

Excellent:4 Very Good:3 Good:2 Average:1

Suggestions if any:



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Department of Electronics and Communication Engineering

Title of the Session: *NI LAB VIEW CLAD*

Department: *21/02/2022 to 25/02/2022. (ECE)*

Date of the Session: *21*

| Sl No. | | Excellent | Very Good | Good | Average |
|--------|---|-----------|-----------|------|---------|
| 1 | The session objectives were stated clearly and met | | ✓ | | |
| 2 | The Workshop was well organized | | ✓ | | |
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Suggestions if any: