



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)
(An UGC Autonomous Institution, Approved by AICTE, New Delhi & Affiliated to JNTUH
Hyderabad), Maisammaguda(H), Medchal-Malkajgiri District, Secunderabad, Telangana State-
500100, www.mrec.ac.in

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 28.02.2022

From

The HoD,
Department of Electrical and Electronics Engineering,
Malla Reddy Engineering College (A),
Secunderabad.

To

The Principal,
Malla Reddy Engineering College (A),
Secunderabad.

Sub: Reg. Request for sanction for conducting a Workshop on "Design of Electrical Circuits
using MATLAB and PSPICE"

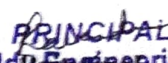
Respected Sir,

As a part of development of departmental PG Students, I request you to sanction the conduction
of Workshop on "Design of Electrical Circuits using MATLAB and PSPICE" for the 2021-22
academic year which starts from 14.03.2022 to 16.03.2022.

Thanking you,


HoD EEE

Head, Dept. of EEE
Malla Reddy Engineering College (A)
Maisammaguda, Dhulapally,
Secunderabad-500100


Principal
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
Dist. Medak, Sec'bad-500100



Workshop
On
Design of Electrical Circuits
using MATLAB and PSPICE
(14.03.2022 to 16.03.2022)

Organized by
Department of EEE
In association with
Indian Society for Technical Education
(ISTE)

MALLA REDDY ENGINEERING COLLEGE (A)

Maisammaguda, Dhulapally, Secunderabad - 500100

REGISTRATION FORM

1. Name :
.....
2. Department :
.....
3. Year of experience:
.....
4. Organization :
.....
5. Address for communication:
.....
.....
6. Phone with STD code :
.....

Mobile :
.....

COMMITTEE

CHIEF PATRON

Sri. Ch. Malla Reddy, M.L.A

Hon'ble Minister Labour & Employment, Factories,
Women & Child Welfare and Skill Development,
Telangana State.
Founder Chairman, Malla Reddy Group of Institutions.

PATRON

Sri. Ch. Mahender Reddy

Secretary, CMR Educational Society

Dr. Ch. Bhadra Reddy

President, CMR Educational Society

Mr. P. Praveen Reddy

Director, MRGI

CO PATRON

Dr. A. Ramaswamy Reddy

Principal, MREC, Hyderabad.

CONVENOR

Dr. N. Rajeswaran

Professor & Head of Department EEE

COORDINATOR

Dr. Raja Reddy, Assoc Professor, (EEE)

Mrs Y Sudha Reddy, Asst Professor (EEE)

For More Information, please contact

us:6300074961,8121621371

eeehod@mrec.ac.in

sudhayellammareddy@gmail.com

A

Workshop

On

**Design of Electrical Circuits using
MATLAB and PSPICE**

(14.03.2022 to 16.03.2022)



Organized by,



Department of EEE

In association with

**Indian Society for Technical Education
(ISTE)**

Malla Reddy Engineering College (A)

(UGC Autonomous Institution, Affiliated to JNTUH)

Maisammaguda, Secunderabad-500100

Telangana, Ph.No:040-64634231

www.mrec.ac.in

About the Institution

Malla Reddy Engineering College (MREC) is a fulfilment of the distinct vision of its founder Chairman, Mr. Ch. Malla Reddy, the present MLA, Medchal Constituency and Hon'ble Minister Labour & Employment, Factories, Women & Child Welfare and Skill Development, Telangana State. MREC is one of the premier Engineering Colleges in the State of Telangana, established in the year 2002, affiliated to Jawaharlal Nehru Technological University Hyderabad (JNTUH), Hyderabad and conferred autonomous status by JNUTH in 2011, UGC in 2014-2020 and 2020-2025. MREC has 11 academic programmes in UG and 7 programmes in PG which is serving the needs of the student body for the last 19 years. The institute is accredited by the National Board of Accreditation (NBA) for 8 programs (5 UG & 3 PG) and National Assessment and Accreditation Council (NAAC) with 'A' grade. It offers B. Tech. Programs in the disciplines of CE, EEE, Mech., ECE, CSE, CSE-AI&ML, CSE-CS, CSE-DS, CSE-IoT, IT and Mining Engineering besides six M. Tech. Programs with SE, EPS, VLSI&ES, TE, MD, CSE specializations and MBA.

About the EEE Department

Department of Electrical and Electronics Engineering was established in the year 2002 and presently it offers under graduate programme in EEE and post graduate programmes in Electrical Power Systems. The UG programme is accredited by National Board of Accreditation (NBA) in the year 2014 for three years and reaccredited in the year 2018 for 3 years. The PG programme is also accredited by NBA in the year 2019 for 3 years.

Vision of the Department

To become a reputed centre for imparting quality education and research in the field of Electrical and Electronics Engineering with human values, ethics and social responsibility.

Mission of the Department

To impart quality education and research to undergraduate and postgraduate students in Electrical and Electronics Engineering.

Overview of the Program

The Programme is about the Design of Electrical Circuits using MATLAB and PSPICE and to manage the employees for Training in different sectors. The main aim of this programme is to make every individual to get an idea on the individual employment, Managing Skills of an Industry or a company, Banking perspectives, Prototype designing of a business and to sustain the Business competition in the world.

Objectives

The aim of this programme is to provide opportunities to student and faculty members to understand about the Design of Electrical Circuits using MATLAB and PSPICE, Banking Perspectives, financing start-ups and Design and development of prototypes. This mainly focuses on the awareness on competitive analysis in business and to overcome the pressures in the business.

Learning Outcome

- Ability to learn about the importance of Design of Electrical Circuits using MATLAB and PSPICE.
- Ability to learn requirements on Start ups.
- Ability to learn on Banking perspectives
- Ability to learn on Planning a Business
- Ability to design the prototypes

Topics to be covered

- An Overview of Design of Electrical Circuits using MATLAB and PSPICE.
- Creativity and Innovation.
- Business model Generation Components and types.
- Complete Analysis of Business.

Resource Person

Dr.A.Jayalaxmi

Participation Certificate

Certificate will be issued to all the participants who attend the program and submitted the feedback.

Registration

No Registration fees

Address For Correspondence

The Co-ordinator,

Department of Electrical & Electronics Engineering,

Malla Reddy Engineering College,

Maisammaguda, Secunderabad- 500100.

Telangana State

E-mail: eeehod@mrec.ac.in



Malla Reddy Engineering College (Autonomous)

Secunderabad, Telangana

Department of Electrical and Electronics Engineering

Workshop


On

Design of Electrical Circuits using MATLAB and PSPICE

14.03.2022 to 16.03.2022

Programme Schedule							
	010.00 - 11.00	11.00 - 11.15	11.15 - 12.45	12.45 - 01.30	01.30 - 03.00	03.00 - 3.15	03.15 - 04.45
Sessions of Day 1	Registration and ice breaking & about the programme	BREAK	Design of Electrical Circuits using MATLAB and PSPICE	LUNCH BREAK	Design of Electrical Circuits using MATLAB and PSPICE	BREAK	Design of Electrical Circuits using MATLAB and PSPICE
	Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi
Sessions of Day 2	Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE
	Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi
Sessions of Day 3	Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE		Design of Electrical Circuits using MATLAB and PSPICE & Valedictory
	Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi		Dr.A.Jayalaxmi


Coordinator


HoD/EEE
Head, Dept. of EEE
Malla Reddy Engg. College (A)
Sisamamaguda, Malakpet, Sec.
Hyd-500100



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

(An UGC Autonomous Institution, Approved by AICTE, New Delhi & Affiliated to JNTUH Hyderabad). Maisammaguda(H), Medchal-Malkajgiri District, Secunderabad, Telangana

State-500100, www.mrec.ac.in

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ELECTRICAL POWER SYSTEMS

List of Students Enrolled

Academic Year 2021-2022

Design of Electrical Circuits using MATLAB and PSPICE

S. No.	Roll Number	Name of the Student
1	20J41D0701	ANNAM SOWMYA
2	20J41D0702	ARUMULLA NAVEEN
3	20J41D0703	BALAGANI JHANSI RANI
4	20J41D0704	BINGI GEETHIKA RANI
5	20J41D0705	BOEEN ESHWAR
6	20J41D0706	BOJJA ABHISHALOM SHANTHI VARDHAN
7	20J41D0707	BOLISHETTY VIVEK VARMA
8	20J41D0708	CHIRUTHA MAITHILI
9	20J41D0709	GOURABOINA RAJESH
10	20J41D0710	JANGAMPALLY VIGNESHWAR GOUD
11	20J41D0711	KAGITHAPU RAVI CHANDRA
12	20J41D0712	KOTTOLA AKHIL
13	20J41D0713	KUMMARI SRAVANI
14	20J41D0714	NARLA KRISHNA KISHORE
15	20J41D0715	NAGURLA AKHILA
16	20J41D0716	NATRAJ MANTENI
17	20J41D0717	PALANATI PRUDVI RAJ
18	20J41D0718	SALIKANTI UDAY
19	20J41D0719	SWATHI .G
20	20J41D0720	V. PRASAD KUMAR RAYA
21	20J41D0721	VELPULA ARAVINDH
22	20J41D0722	VODAPALLY RAJA
23	20J41D0723	YENUGA SAIJYOTHIRMAY


HOD-EEE

EEE DEPARTMENT
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100.

Malla Reddy Engineering College (Autonomous)
Department of Electrical and Electronics Engineering
Workshop

On

"Design of Electrical Circuits using MATLAB and PSPICE"

Attendance Sheet from 14.03.2022 to 16.03.2022

Sl. No.	Name of Participant	Day 1		Day 2		Day 3	
		FN	AN	FN	AN	FN	AN
1	ANNAM SOWMYA	Sy	Sy	Sy	Sy	Sy	Sy
2	ARUMULLA NAVEEN	A	A	Mm	Mm	Mm	Mm
3	BALAGANI JHANSI RANI	Tham	Tham	Tham	Tham	Tham	Tham
4	BINGI GEETHIKA RANI	Ge	Ge	Ge	Ge	Ge	Ge
5	BOEEN ESHWAR	Gsh	Gsh	Gsh	Gsh	Gsh	Gsh
6	BOJJA ABHISHALOM SHANTHI VARDHAN	Shanthi	Shanthi	Shanthi	Shanthi	Shanthi	Shanthi
7	BOLISHETTY VIVEK VARMA	Vv	Vv	Vv	Vv	Vv	Vv
8	CHIRUTHA MAITHILI	Mai	Mai	Mai	Mai	Mai	Mai
9	GOURABOINA RAJESH	Rajesh	Rajesh	Rajesh	Rajesh	Rajesh	Rajesh
10	JANGAMPALLY VIGNESHWAR GOUD	Vg	Vg	Vg	Vg	Vg	Vg
11	KAGITHAPU RAVI CHANDRA	RC	RC	RC	RC	RC	RC
12	KOTTOLA AKHIL	Ak	Ak	Ak	Ak	Ak	Ak
13	KUMMARI SRAVANI	Sra	Sra	Sra	Sra	Sra	Sra
14	NARLA KRISHNA KISHORE	Kk	Kk	Kk	Kk	Kk	Kk
15	NAGURLA AKHILA	Akh	Akh	Akh	Akh	Akh	Akh
16	NATRAJ MANTENI	Nm	Nm	Nm	Nm	Nm	Nm
17	PALANATI PRUDVI RAJ	Pr	Pr	A	A	Pr	Pr
18	SALIKANTI UDAY	Uday	Uday	Uday	Uday	Uday	Uday
19	SWATHI .G	Sw	Sw	Sw	Sw	Sw	Sw
20	V. PRASAD KUMAR RAYA	Vp	Vp	Vp	Vp	Vp	Vp
21	VELPULA ARAVINDH	Ar	Ar	Ar	Ar	Ar	Ar
22	VODAPALLY RAJA	Raja	Raja	Raja	Raja	A	A
23	YENUGA SAIJYOTHIRMAY	Ys	Ys	Ys	Ys	Ys	Ys


Coordinator


HOD/EEE

EEE DEPARTMENT
Malla Reddy Engineering College
(Autonomous)
 Maisammaguda, Dilulapally,
 (Post Via Kompally), Sec'bad-500 100.



MALLA REDDY ENGINEERING COLLEGE

(UGC Autonomous Institution, Approved by AICTE, New Delhi & Affiliated to
JNTUH, Hyderabad)

Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,
Telangana State – 500100, www.mrec.ac.in



Participation Certificate

This is to certify that Mr/[✓]Ms/Mrs/Dr, G. SWATHI

Department of **EEE**, M.TECH - E.P.S. has attended Workshop on Design of Electrical Circuits using MATLAB and PSPICE from 14.03.2022 to 16.03.2022 organized by Department of **Electrical and Electronics Engineering in Association with Indian Society for Technical Education(ISTE)**, Malla Reddy Engineering College, Secunderabad.


Coordinator


Convener / HOD


Principal

MALLA REDDY ENGINEERING COLLEGE(A)

Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100

Department of Electrical and Electronics Engineering

Indian Society for Technical Education

(ISTE)

Organised

Three Day

Workshop

On

Design of Electrical Circuits using MATLAB and PSPICE

14.03.2022 to 16.03.2022


A Brief Report


To meet the Industry goals and requirements, the Department of Electrical and Electronics Engineering organized a Workshop on “Design of Electrical Circuits using MATLAB and PSPICE” during 14.03.2022 to 16.03.2022. The inaugural ceremony of the Workshop commenced with the lighting of lamp by a group of dignitaries. The inaugural ceremony was attended by **Chief Guest Sri. Ch. Malla Reddy**, Hon’ble Minister Labour & Employment, Factories, Women & Child Welfare and Skill Development, Telangana, along with Hon’ble Principal of MREC(A), **Dr.A.Ramaswamy Reddy**, **Keynote speaker Dr.A.Jayalaxmi**, Professor, JNTU Hyderabad, Convener of the programme **Dr. N. Rajeswaran**, Deans and Heads of various departments, members of faculty, press and participants from other Engineering institutions and Industries across different states of the country.

The programme was interactive and had both theory and practical sessions. The objective of the Workshop is to present theoretical and practical concepts with an emphasis on various applications. The resource person of the Workshop: **Dr.A.Jayalaxmi**, Professor, JNTU Hyderabad delivered the sessions on Design of Electrical Circuits using MATLAB and PSPICE. All the lectures were very well appreciated by the participants and interaction with the resource person was very good. This Workshop is useful for students in doing their research work effectively and for their career development.

The Workshop was a great success. A feedback session was conducted and participants expressed their opinions. Their comments were very positive. The session concluded with the valedictory function. **Dr. A. Ramaswamy Reddy**, Principal, MREC (A), **Dr.A.Jayalaxmi** and **Dr. N. Rajeswaran**, HOD, Electrical and Electronics Engineering department were present in the valedictory function and distributed the certificates to the participants. The general opinion of all the participants was that such programme must be conducted periodically for the benefit of staff and students.


Coordinator


Principal
Malla Reddy Engineering College (A)
Maisammaguda, Dhulapally,
Secunderabad-500100


Principal
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
Medchal-Malkajgiri, Sec'bad-500100



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)
(An UGC Autonomous Institution, Approved by AICTE, New Delhi & Affiliated to JNTUH Hyderabad). Maisammaguda(H), Medchal-Malkajgiri District, Secunderabad, Telangana State-500100, www.mrec.ac.in

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Name of the Faculty:

Department & Designation: *EEE/EPs*

Date of the Session: *14.03.2022 to 16.03.2022*

Title of the Session: *Design of electrical circuits using MATLAB*

Time:

S.No		Excellent	Very Good	Good	Average
1	The Session Objective were stated clearly and met		✓		
2.	The Workshop was well Organized	✓			
3.	The information and / or skills presented were relevant and useful for Career		✓		
4	The presenter responded to questions effectively?	✓			
5	The overall assessment of the Session	✓			
6	The session content met your Expectations	✓			

Excellent:4 Very Good:3 Good:2 Average:1

Suggestions If any: —