	<p align="center">MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)</p> <p align="center">(UGC Autonomous Institution, Affiliated to JNTUH, Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP-- II S.C. 1.1)</p> <p align="center">Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100</p>	<p align="center">Department of ECE</p>
		<p align="center">Course: M.Tech</p>
		<p align="center">Date: 19.12.2022 to 23.12.2022</p>

Department of Electronics and Communication Engineering


Academic Year 2022-2023

Name of the Value Added Course: VLSI Design and Verification

List of Students enrolled for value added course

Sl. No.	Roll No.	Name	Year/Semester
1	21J41D6801	UJELLI RAKESH REDDY	M. Tech II/I
2	21J41D6802	PONNA SNEHA	M. Tech II/I
3	21J41D6803	TAGORE SANJANA	M. Tech II/I
4	21J41D6804	M. INDRANI SOWMYA	M. Tech II/I
5	21J41D6805	PAMPARI SRAVANTHI	M. Tech II/I
6	21J41D6806	NAGAM SHRAVYA	M. Tech II/I
7	21J41D6807	JANGILI SAIKIRAN	M. Tech II/I
8	21J41D6808	BALERAO CHOUDARY MANISH	M. Tech II/I
9	21J41D6809	KONATHAM MOUNIKA	M. Tech II/I


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 Malla Reddy Engineering College
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