

(54) Title of the invention : CASCADED MULTILEVEL PV INVERTER WITH IMPROVED HARMONIC PERFORMANCE DURING POWER IMBALANCE BETWEEN POWER CELLS

(51) International classification :H02M7/483, H02M7/5387, H02M1/00, H02M1/12, H02J3/01, G05F1/67, G06F30/30, G06F30/367

(86) International Application No :NA
Filing Date :NA

(87) International Publication No : NA

(61) Patent of Addition to Application Number :NA
Filing Date :NA

(62) Divisional to Application Number :NA
Filing Date :NA

(71)Name of Applicant :
1)Malla Reddy Engineering College
 Address of Applicant :Malla Reddy Engineering College Dhulapally post via Kompally Maisammaguda Secunderabad -500100 Secunderabad -----
2)Y Sudha
Name of Applicant : NA
Address of Applicant : NA

(72)Name of Inventor :
1)Y Sudha
 Address of Applicant :Assistant Professor Electrical and Electronics Engineering Dept., Malla Reddy Engineering College, Maisammaguda (Post. Via. Kompally), Mechal-Malkajgiri-500100. State:Telangana Email ID & Contact Number: yelmareddy.sudha@gmail.com& 8121621371 Secunderabad -----
2)SAFIA SHAIK
 Address of Applicant :Assistant Professor Electronics and Communication Engineering Dept Maulana Azad National Urdu University Hyderabad-500032 State:Telangana Email ID & Contact Number:safiashaik@manuu.edu.in & 7032242315 Hyderabad -----
3)Dr.G.Vijay kumar
 Address of Applicant :Professor, Department of Mechanical Engineering, Nadimpalli Satyanarayana Raju .Institute of Technology(NSRIT), Sontyam, Pendurthi, Visakhapatnam-531173 State:Andhra Pradesh Email ID: drgvkumar.me@nsrit.edu.in Contact Number : 8897119811 Visakhapatnam -----
4)G Ravi kishore
 Address of Applicant :Assistant Professor Electrical and Electronics Engineering Dept., Dept., VJIT Hyderabad-500075 State:Telangana Email ID & Contact Number:ravikishore@vjit.ac.in & 8500000056 Hyderabad -----
5)Dr.V.Ananda Babu
 Address of Applicant :Associate Professor Department of Mechanical Engineering, Nadimpalli Satyanarayana Raju .Institute of Technology(NSRIT), Sontyam, Pendurthi, Visakhapatnam-531173 State:Andhra Pradesh Email ID: dranandababur.me@nsrit.edu.in Visakhapatnam -----
6)Mr. N Rajesh Babu
 Address of Applicant :Assistant Professor Electrical and Electronics Engineering Dept., PSCMR CET,Vijayawada-520001, State: Andhra Pradesh Vijayawada -----
7)A.Ramakrishna
 Address of Applicant :Associate Professor Department of Electrical and Electronics Engineering Bonam Venkata Chalamayya Engineering College(A), Odalarevu, Amalapuram, AndhraPradesh 533210 Amalapuram -----
8)G.Lova Raju
 Address of Applicant :Associate Professor Department of Electrical and Electronics Engineering Bonam Venkata Chalamayya Engineering College(A), Odalarevu, Amalapuram, AndhraPradesh 533210 Amalapuram -----

(57) Abstract :
 ABSTRACT In cascaded multilevel converters, variations in power cell irradiances lead to variations in duty cycles among those cells when preserving maximum power point tracking (MPPT). Since it is proportionate to the output voltage and current distortions, the difference in cell duty cycles is undesirable. In order to address this, a multilevel architecture for photovoltaic (PV) applications is suggested, in which a H6 bridge power cell is employed rather than an H-bridge one. The suggested converter maintains MPPT operation by injecting power at a lower voltage from the shaded cells when there is a discrepancy in solar irradiation between the power cells. This change enables us to maintain good output voltage and current waveform quality by enabling all power cells to maintain an equal duty cycle regardless of the weather. An experimental prototype and a comprehensive simulation model are constructed in order to evaluate the efficacy of the suggested remedy. The acquired data demonstrate that, in comparison to the cascaded H-bridge, the suggested topology offers noticeably better output voltage and current characteristics.

No. of Pages : 7 No. of Claims : 2