

MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

Department of Electronics and Communication Engineering

TIME TABLE

Class Incharge

Ms.S. Bhagya Lakshmi

Academic Year : 2020 - 2021

With Effective From : 27-11-2020, 21-01-2021

Class/Year/Semester : B.Tech III Year/V Semester - A SEC

Period	I	II	11:20-11:30	III	IV
Day/Time	9:30-10:30	10:30-11:30		11:30 - 12:30	12:30 - 1:30
Monday	MP &MC Lab		B R E A K	MP &MC Lab	
Tuesday	LDICA Lab			LDICA Lab	
Wednesday	DCCN Lab			DCCN Lab	
Thursday	MP &MC Lab			MP &MC Lab	
Friday	LDICA Lab			LDICA Lab	
Saturday	DCCN Lab			DCCN Lab	

Sub Code	Theory/Practical	Hrs	Staff in-charge
80417	Microprocessor and Micro Controller Lab	8	Mrs. G. Jyothi, Mrs. Anuradha
80418	Linear & Digital Integrated Circuit Applications Lab	8	Ms. G Krishnaveni, Ms. B. Kalpana, Ms. Maheshwari D
80419	Data Communication and Computer Applications Lab	8	Mrs. B. Sai Prasanna, Mr. A. Narasimha Reddy
Total		24	


Time Table Incharge


HOD


Director/Principal

MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

Department of Electronics and Communication Engineering

TIME TABLE

Class Incharge

Mrs.C. Silpa

Academic Year : 2020 - 2021

With Effective From : 27-11-2020, 21-01-2021

Class/Year/Semester : B.Tech III Year /V Semester - B SEC

Period	I	II	11:20-11:30	III	IV
Day/Time	9:30-10:30	10:30-11:30		11:30 - 12:30	12:30 - 1:30
Monday	LDICA Lab		B R E A K	LDICA Lab	
Tuesday	DCCN Lab			DCCN Lab	
Wednesday	MP &MC Lab			MP &MC Lab	
Thursday	LDICA Lab			LDICA Lab	
Friday	DCCN Lab			DCCN Lab	
Saturday	MP &MC Lab			MP &MC Lab	

Sub Code	Theory/Practical	Hrs	Staff in-charge
80417	Microprocessor and Micro Controller Lab	8	Mrs. Anuradha, Mr. P. Sandeep Kumar
80418	Linear & Digital Integrated Circuit Applications Lab	8	Ms. G Krishnaveni, Ms. B. Kalpana, Ms. Maheshwari Devi
80419	Data Communication and Computer Applications Lab	8	Mr. A. Narasimha Reddy , Mr. Y. Murali
Total		24	

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HOD

Director/Principal

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Class Incharge

Mrs.N, Durga Sowdamini

Academic Year : 2020 - 2021

With Effective From : 27-11-2020, 21-01-2021

Class/Year/Semester : B.Tech III Year/V Semester - C SEC

Period	I	II	11:20-11:30	III	IV
Day/Time	9:30-10:30	10:30-11:30		11:30 - 12:30	12:30 - 1:30
Monday	DCCN Lab		B R E A K	DCCN Lab	
Tuesday	MP &MC Lab			MP &MC Lab	
Wednesday	LDICA Lab			LDICA Lab	
Thursday	DCCN Lab			DCCN Lab	
Friday	MP &MC Lab			MP &MC Lab	
Saturday	LDICA Lab			LDICA Lab	

Sub Code	Theory/Practical	Hrs	Staff In-charge
80417	Microprocessor and Micro Controller Lab	8	Mr. P. Sandeep Kumar, Mrs. G. Jyothi,
80418	Linear & Digital Integrated Circuit Applications Lab	8	Ms. G Krishnaveni, Ms. B. Kalpana, Ms. Maheshwari Devi
80419	Data Communication and Computer Applications Lab	8	Mr. Y. Murali, Mrs. B. Sai Prasanna
Total		24	


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