

# MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

Department of Electronics and Communication Engineering

## TIME TABLE

Class Incharge

Academic Year : 2020 - 2021

Class/Year/Semester : B.Tech IV Year/VII Semester - A SEC


Mr.M. Kranthi Kumar

With Effective From :21-02-2020, 20-11-2020

Period	I	II	III	IV
Day/Time	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 1:30
Monday	VLSI LAB		VLSI LAB	
Tuesday	MWE & DC LAB		MWE & DC LAB	
Wednesday	VLSI LAB		VLSI LAB	
Thursday	MWE & DC LAB		MWE & DC LAB	
Friday	VLSI LAB		VLSI LAB	
Saturday	MWE & DC LAB		MWE & DC LAB	

Sub Code	Theory/Practical	Hrs	Staff in-charge
70427	Microwave Engineering & Digital Communications Lab	12	Mr. N Srikanth Prasad, Dr. Sudhakar Anjaneyulu
70428	VLSI Lab	12	Dr. J. Sriisha, Ms. K. Santhi, Mr. M. Kranthi Kumar.
	Total	24	

  
Time Table Incharge

  
HOD

  
Director

# MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

## Department of Electronics and Communication Engineering

### TIME TABLE

Class Incharge (s)

Academic Year : 2020 - 2021

Class/Year/Semester : B.Tech IV Year/VII Semester - B SEC

Vuduthala Srinivas

With Effective From :21-12-2020, 20-01-2021

Period	I	II	III	IV
Day/Time	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 1:30
Monday	MWE & DC LAB		MWE & DC LAB	
Tuesday	VLSI LAB		VLSI LAB	
Wednesday	MWE & DC LAB		MWE & DC LAB	
Thursday	VLSI LAB		VLSI LAB	
Friday	MWE & DC LAB		MWE & DC LAB	
Saturday	VLSI LAB		VLSI LAB	

Sub Code	Theory/Practical	Hrs	Staff in-charge
70427	Microwave Engineering & Digital Communications Lab	12	Ms. N Haritha, Mr. Chand Pasha
70428	VLSI Lab	12	Mr. Vudutala Srinivas, Dr. Sima Sahu, Mr. Gosukonda Lalitha, Mr. J Ramesh
	<b>Total</b>	<b>24</b>	

  
Time Table Incharge

  
HOD

  
Director/Principal

# MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

## Department of Electronics and Communication Engineering

### TIME TABLE

Class Incharge

Academic Year : 2020 - 2021

Class/Year/Semester : B.Tech IV Year/VII Semester - C SEC

N. Pandu Ranga Reddy

With Effective From :21-12-2020, 20-01-2021

Period	I	II	III	IV
Day/Time	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 1:30
Monday	VLSI LAB		VLSI LAB	
Tuesday	MWE & DC LAB		MWE & DC LAB	
Wednesday	VLSI LAB		VLSI LAB	
Thursday	MWE & DC LAB		MWE & DC LAB	
Friday	VLSI LAB		VLSI LAB	
Saturday	MWE & DC LAB		MWE & DC LAB	

Sub Code	Theory/Practical	Hrs	Staff in-charge
70427	Microwave Engineering & Digital Communications Lab	12	Mr. N. Pandu Ranga Reddy, Mr. D.Venkata Rami Reddy
70428	VLSI Lab	12	Dr. Sk Fairouz, Mr. Vutkuri srinivas, Mr. Umesh Chandra
	Total	24	

  
Time Table Incharge

  
HOD

  
Director/Principal