



Malla Reddy Engineering College

(AUTONOMOUS)

(An UGC Autonomous Institution Approved by AICTE and Affiliated to JNTU Hyderabad. Accredited by NAAC with 'A' Grade (II - Cycle))

REPORT ON ONE WEEK WORKSHOP ON "VLSI Semi Custom Design"

The department of ECE has organized one week work shop on "VLSI Semi Custom Design" during 18th June 2018 to 23rd June 2018 at VLSI lab in ECE department from 9.30 A.M to 4.30 P.M. The department has invited the resource persons from M/s Vedic school of VLSI Design, Hyderabad.

During the first day, introduction to EDA's, VLSI design flow and front end design tools used for VLSI have been discussed. During the next two days, a detailed description of CAD tools was given with a practical approach. The hardware description language such as VHDL and Verilog are discussed in detail.

On next day, timing analysis and layout have been demonstrated and hands on training have been given to all the students. During last two days of work shop, schematic to layout conversion, DRC checking and final product evaluation have been demonstrated by the resource persons and personally carried out by each and every student.

At an about 200 students of final year ECE have attended and interacted with the resource persons. This work shop was coordinated by N.Pandu Ranga Reddy, Mr.T.Surender Reddy and Mr.V.Srinivas.




CO-ORDINATOR


HEAD, ECE DEPARTMENT


PRINCIPAL



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