

MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

(Affiliated to JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD)
GundlapochampALLY (H), Maisammaguda (V), Medchal (M), Medchal-Malkajgiri (Dist), Hyderabad

IV B.TECH I SEMESTER SUPPLEMENTARY EXAMINATIONS, APRIL-2019Subject: Design Patterns

Branch: CSE

Time: 3 hours

Max. Marks: 75

Answer any FIVE Questions of the following

5x15 Marks= 75 Marks

1. a) Describe organization of design pattern catalog. Draw a diagram to show relationships among design patterns [8M]
b) What is Design pattern? How to use a design pattern [7M]
2. Explain in detail about Lexi's spelling checking and Hyphenation problem [15M]
3. a) What are the issues that arise when applying Factory method pattern? How do you address them [5M]
b) Discuss the intent, applicability, and use of Builder pattern [10M]
4. a) What are the consequences of Adapter? Explain [8M]
b) What are the main issues to consider when implementing the composite pattern [7M]
5. a) Discuss about Flyweight design pattern [8M]
b) Explain sample code and known uses of Façade design pattern [7M]
6. a) Describe implementation of Iterator design pattern [7M]
b) Explain about sample code of Interpreter design pattern [8M]
7. a) Briefly discuss about State design pattern [8M]
b) Describe sample code of Visitor design pattern [7M]
8. Explain the following
a) Refactoring b) Patterns in software c) A common design vocabulary [5M+5M+5M]